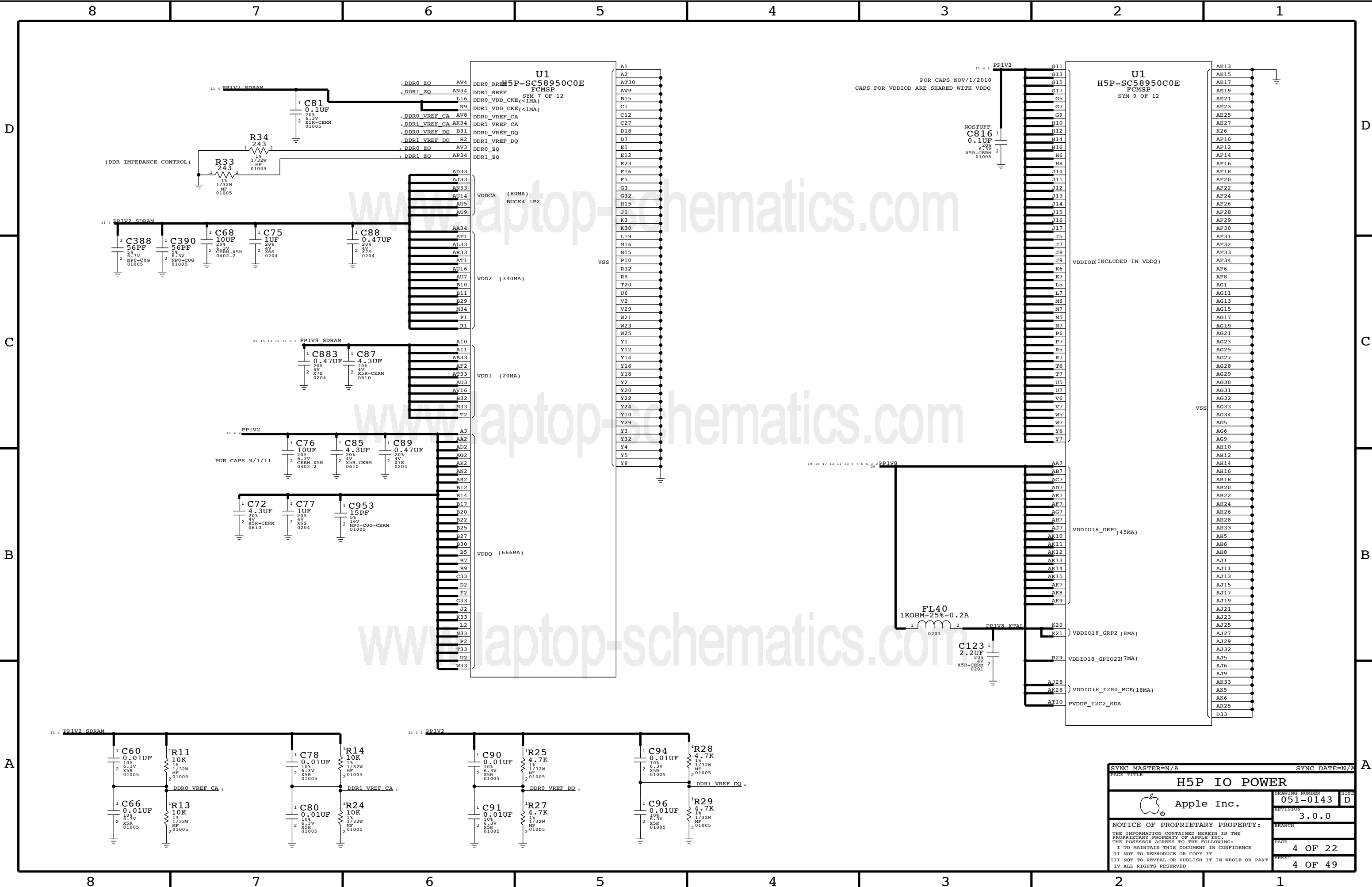
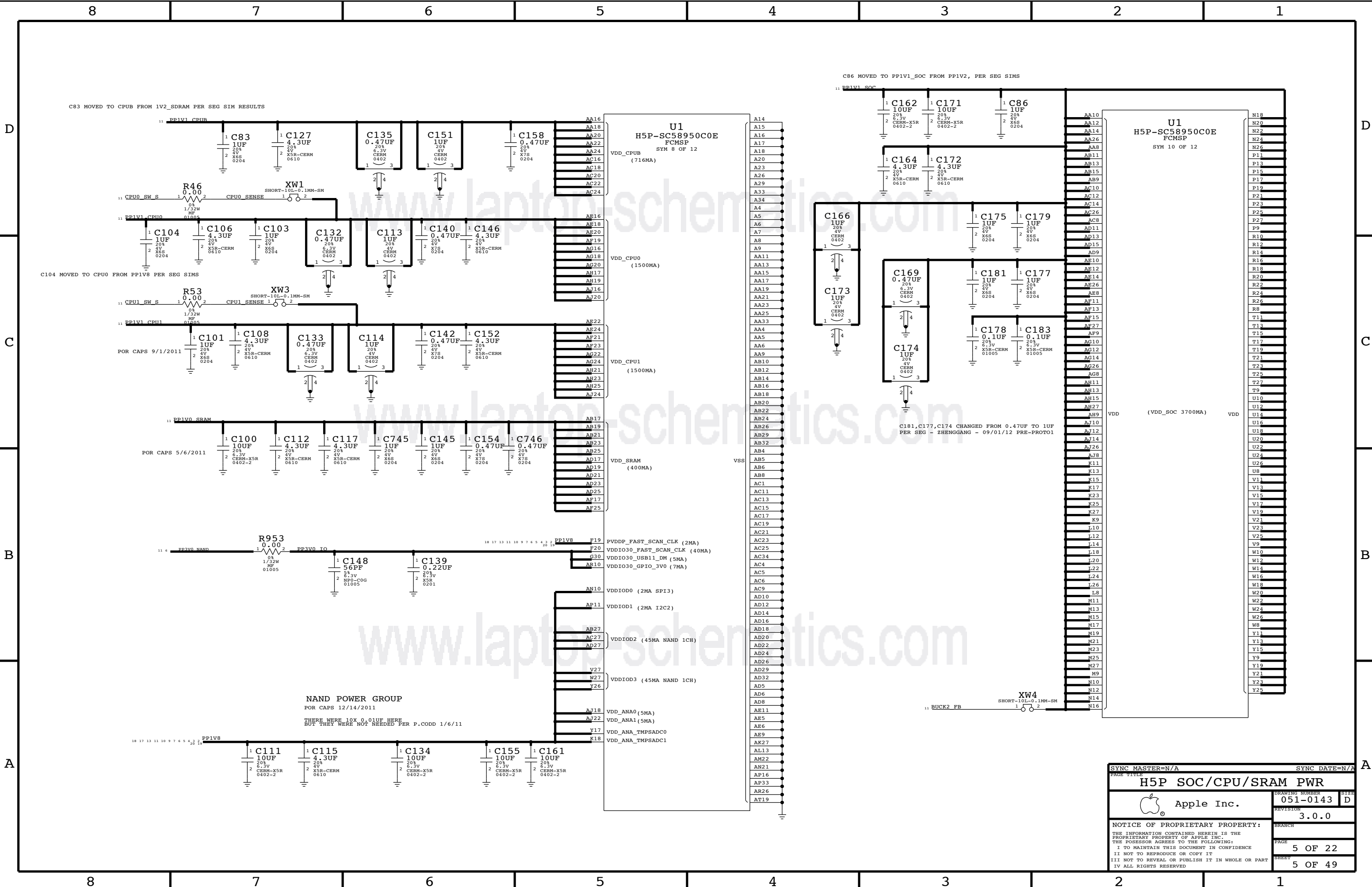
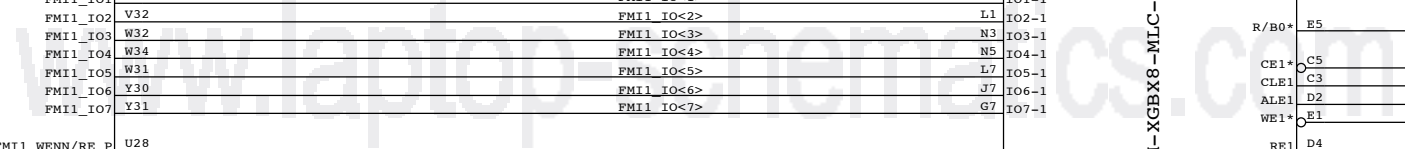




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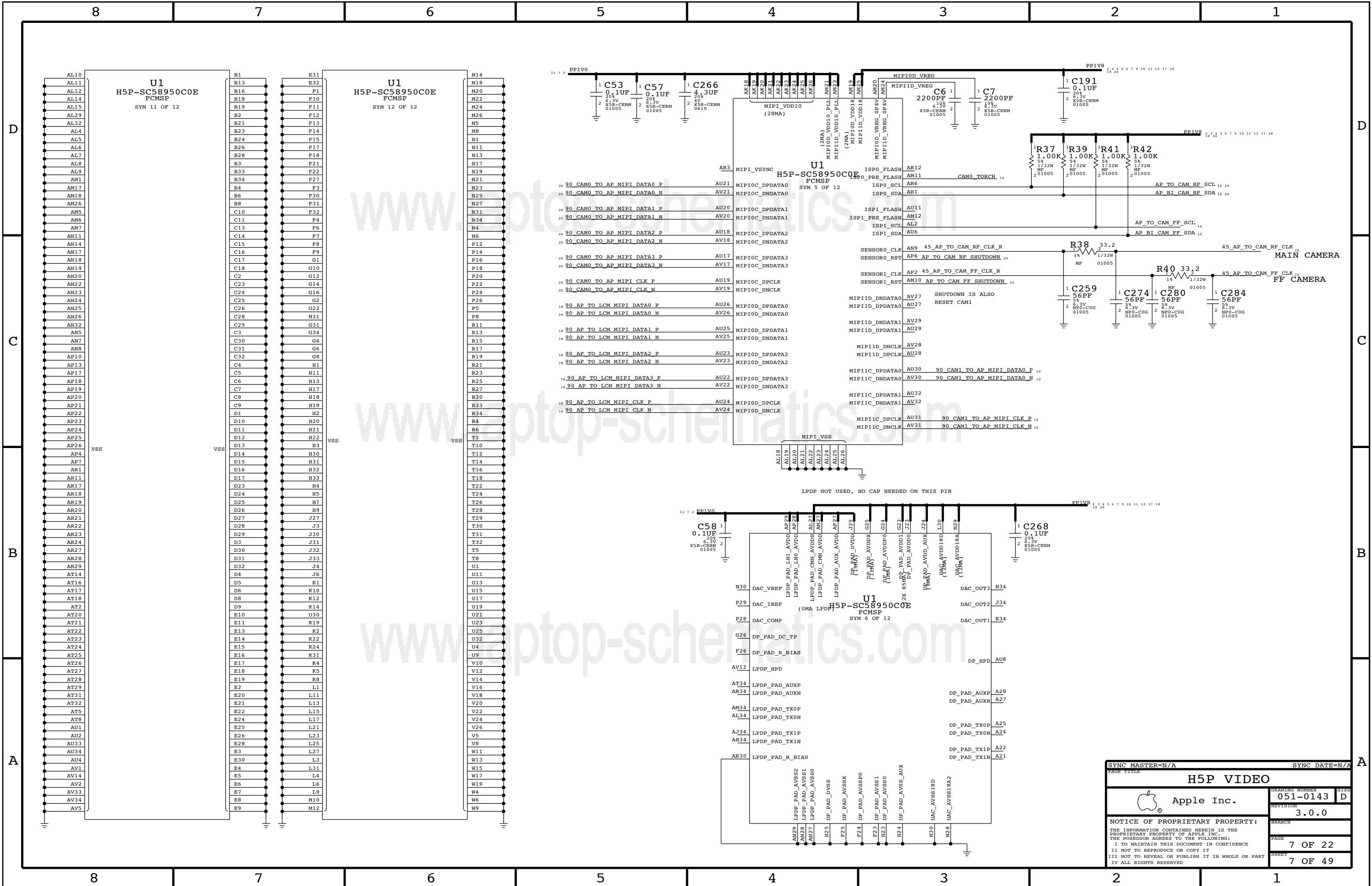
NOTE: HARD PADS SHOULD BE SHIELDED FROM TRACES WITH A GROUND PLANE

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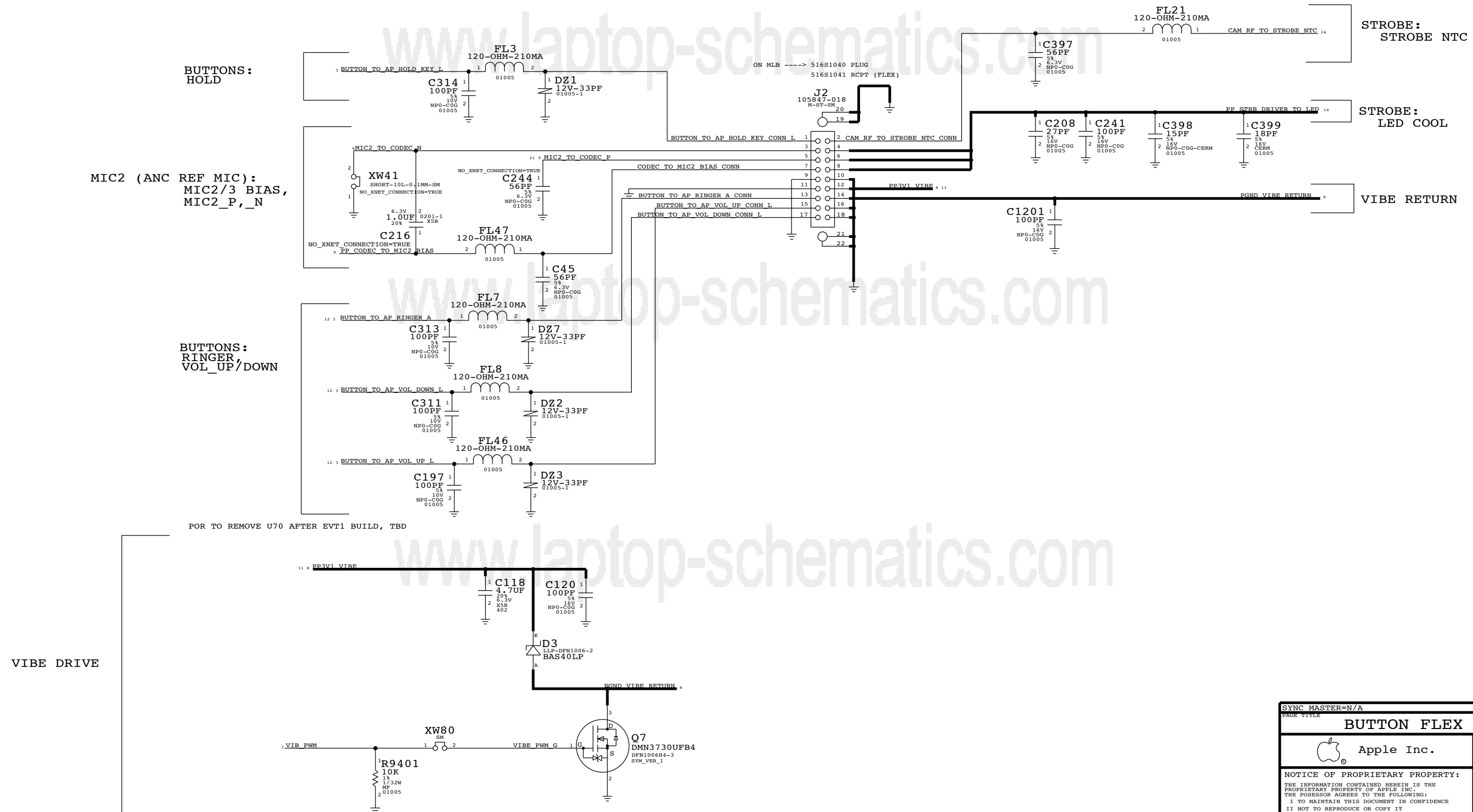
PP2P2MM-NSMSM 1 FMIO IQ<0> 6
PP3P2MM-NSMSM 1 45 FMIO RE L 6
PP14P2MM-NSMSM 1 45 FMIO DOS 6

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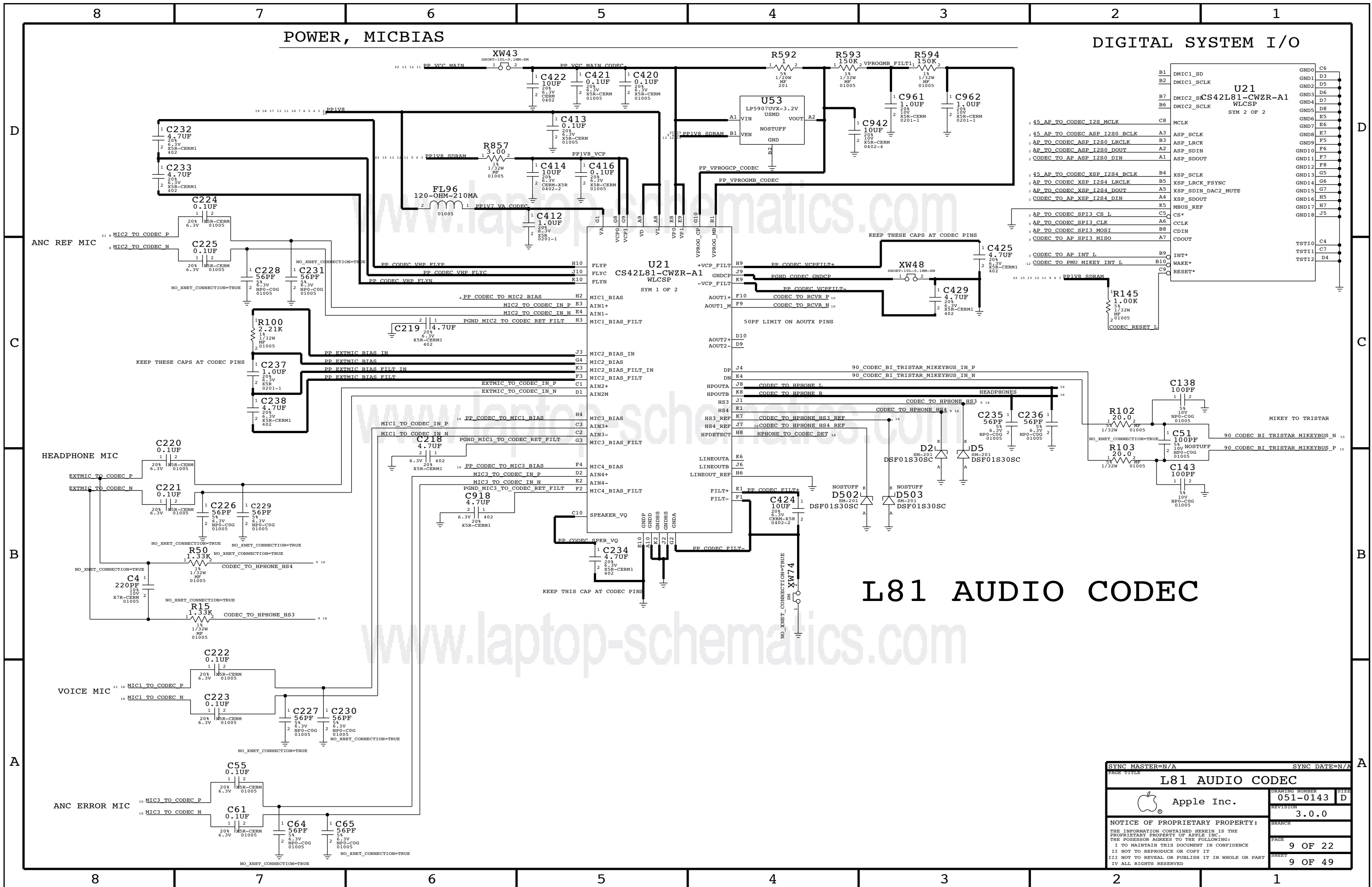
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		SHEET	
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BUTTON FLEX (VIBE DRIVER, BUTTONS, ANC REF MIC, STROBE, STROBE_NTC)



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PAGE TITLE			
BUTTON FLEX B2B			
 Apple Inc.		DRAWING NUMBER	051-0143
		REVISION	3.0.0
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L81 AUDIO CODEC

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PAGE TITLE			
L81 AUDIO CODEC			
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	REVISION	3.0.0	
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CG FLEX B2B

(FF CAM, PROX, ALS, RECEIVER, ANC ERROR MIC)

D

FRONT CAM:
CLK, I2C, SHDN

C

PROX: POWER,
RX, RX_EN

B

ALS: POWER,
I2C, INT

A

RECEIVER

D

FRONT CAM:
POWER AND MIPI

C

MIC3
(ANC ERROR MIC)

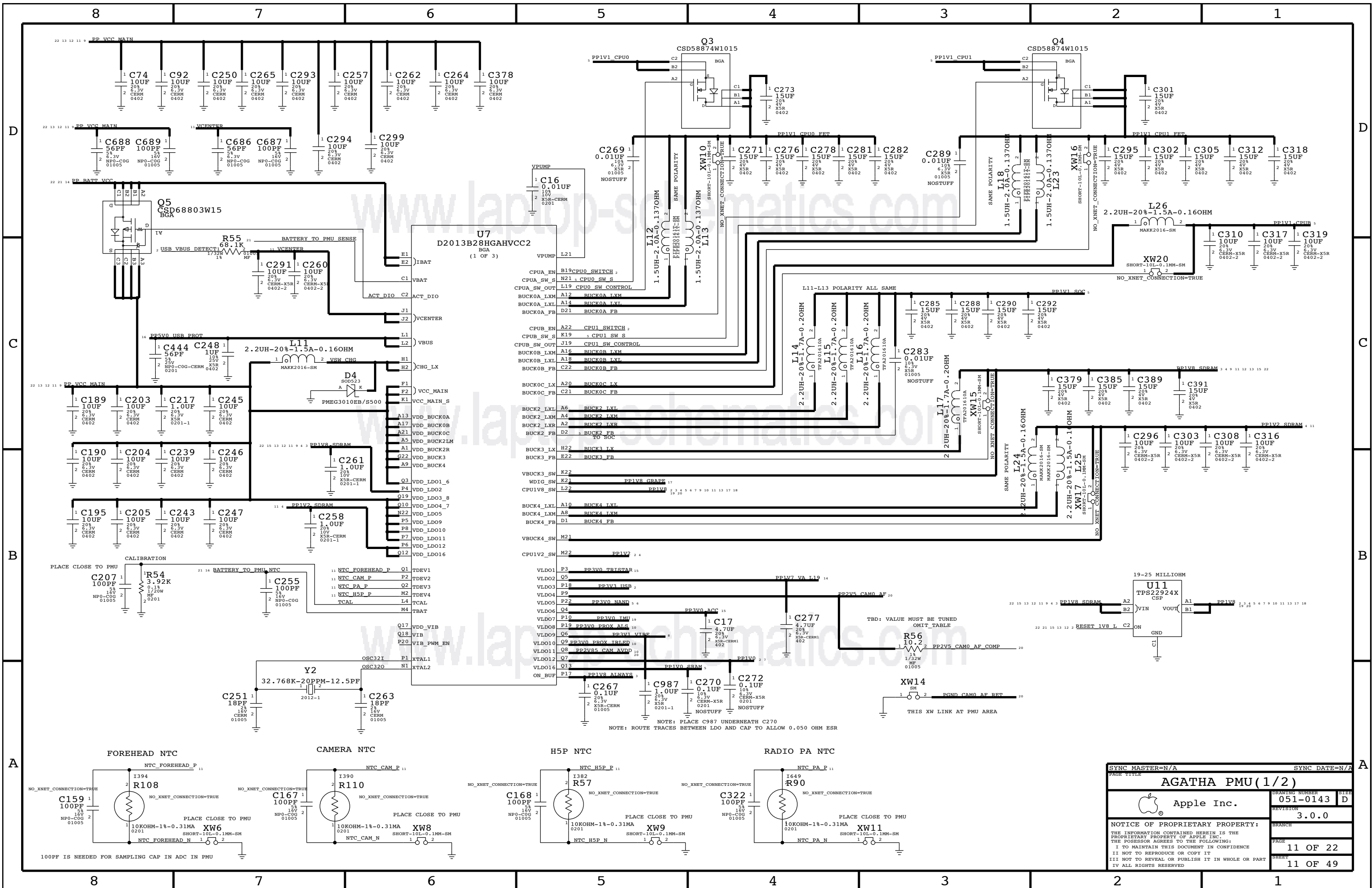
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CAM1 ALS INT

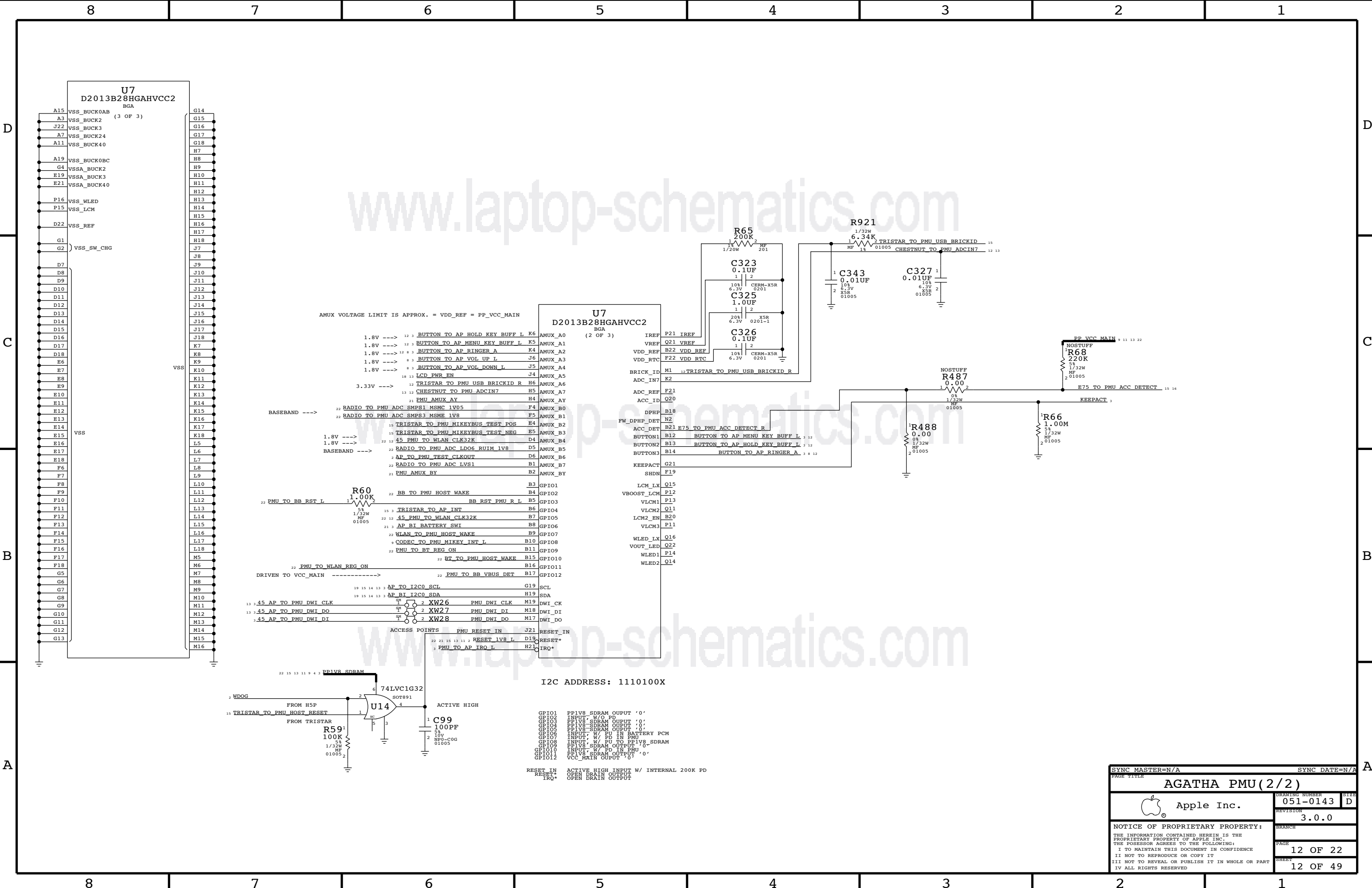
PROX: PWR, TX EN

A

PAGE TITLE		PAGE NUMBER	
CG FLEX B2B		051-0143	
Apple Inc.		3.0.0	
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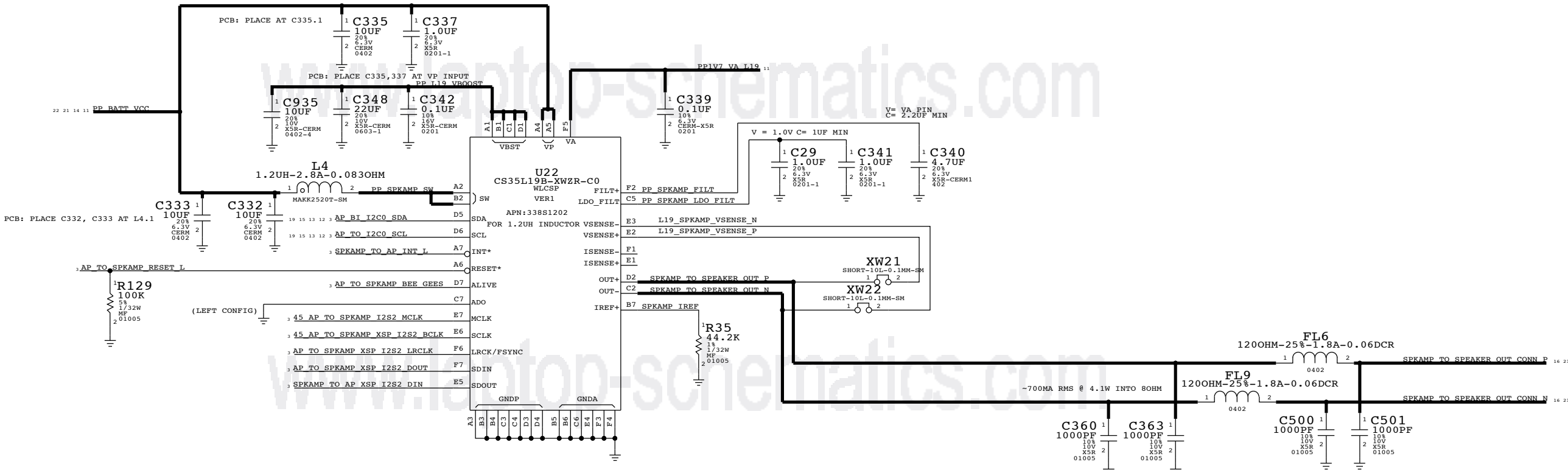
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SPEAKER AMP, LED DRIVER

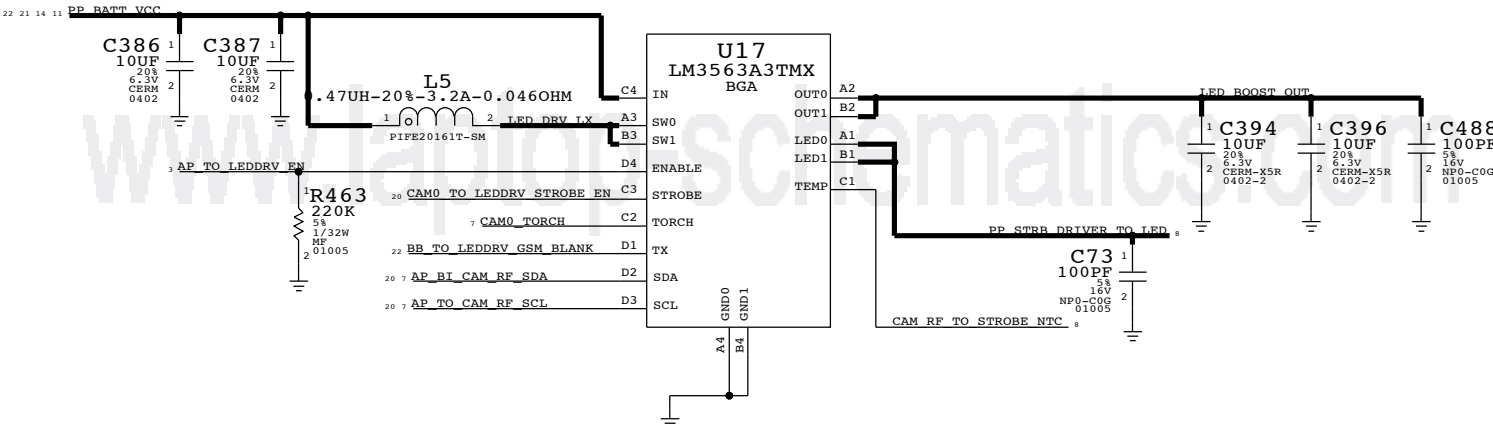
SPEAKER AMP L19

I2C ADDRESS: 1000000X



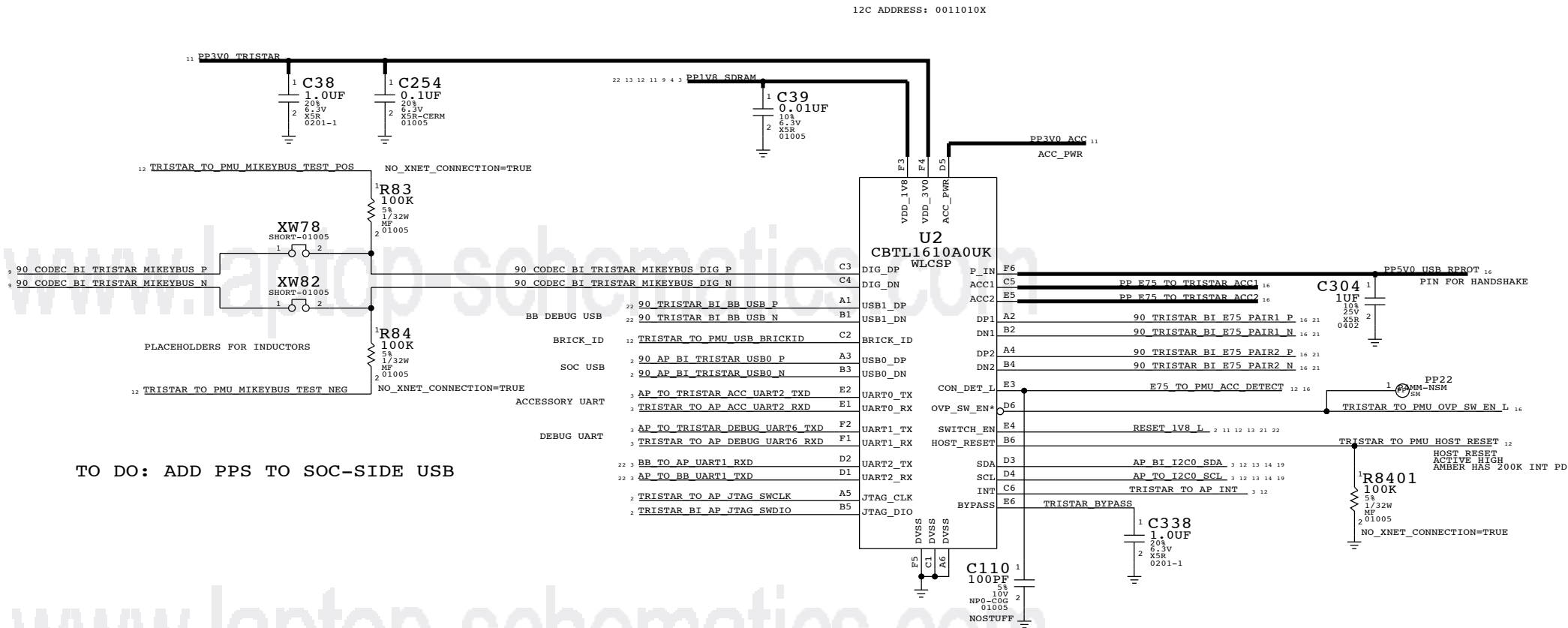
LED DRIVER

I2C ADDRESS: 1100011X



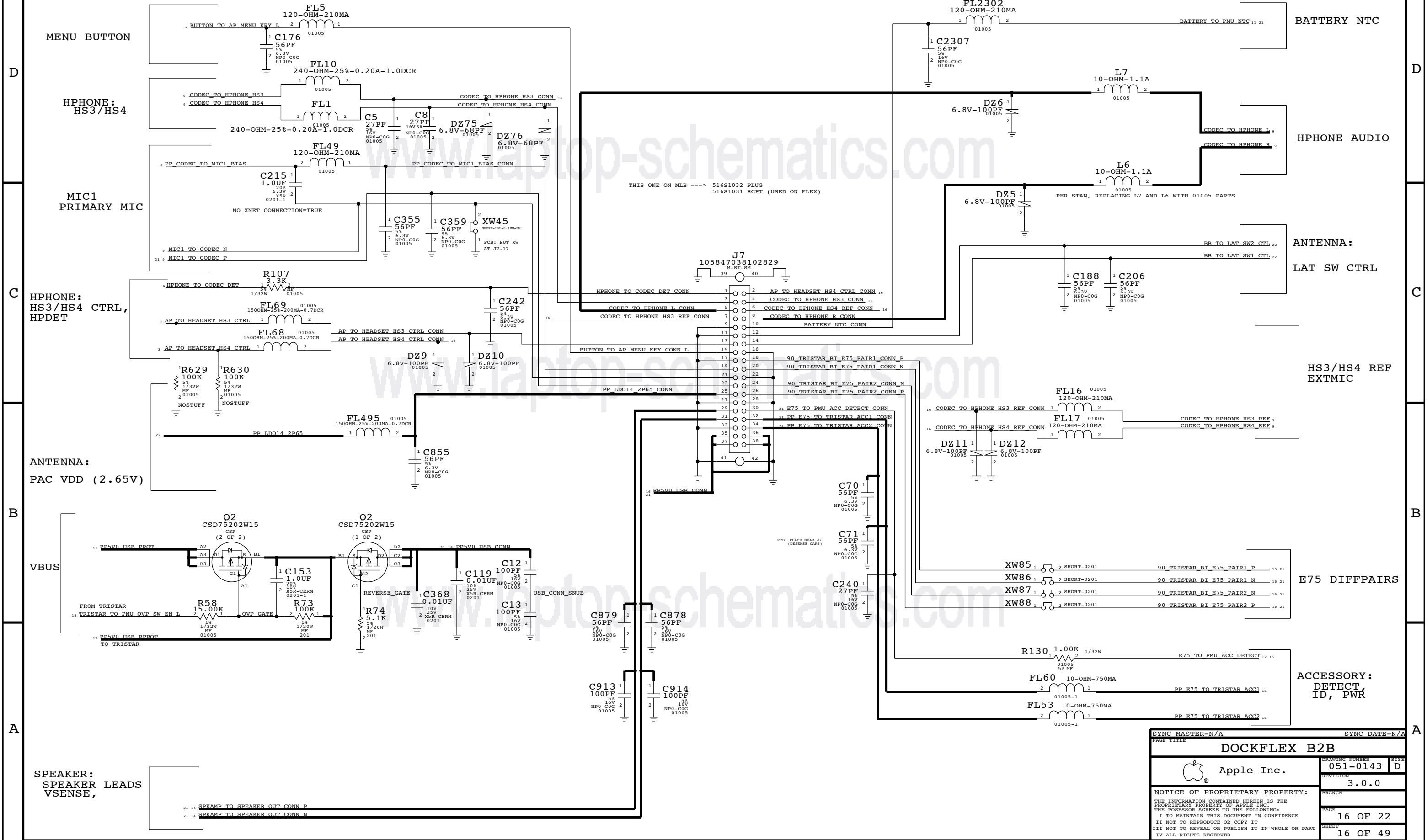
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SPKR AMP + LED DRIVER		DRAWING NUMBER	
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DOCKFLEX B2B (USB VBUS, MENU BTN, SPEAKER, HP, HP EXTMIC, NAVAJO, ANTENNA PAC/LAT SW CTRL, MIC1 (VOICE MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)

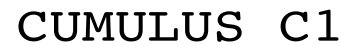


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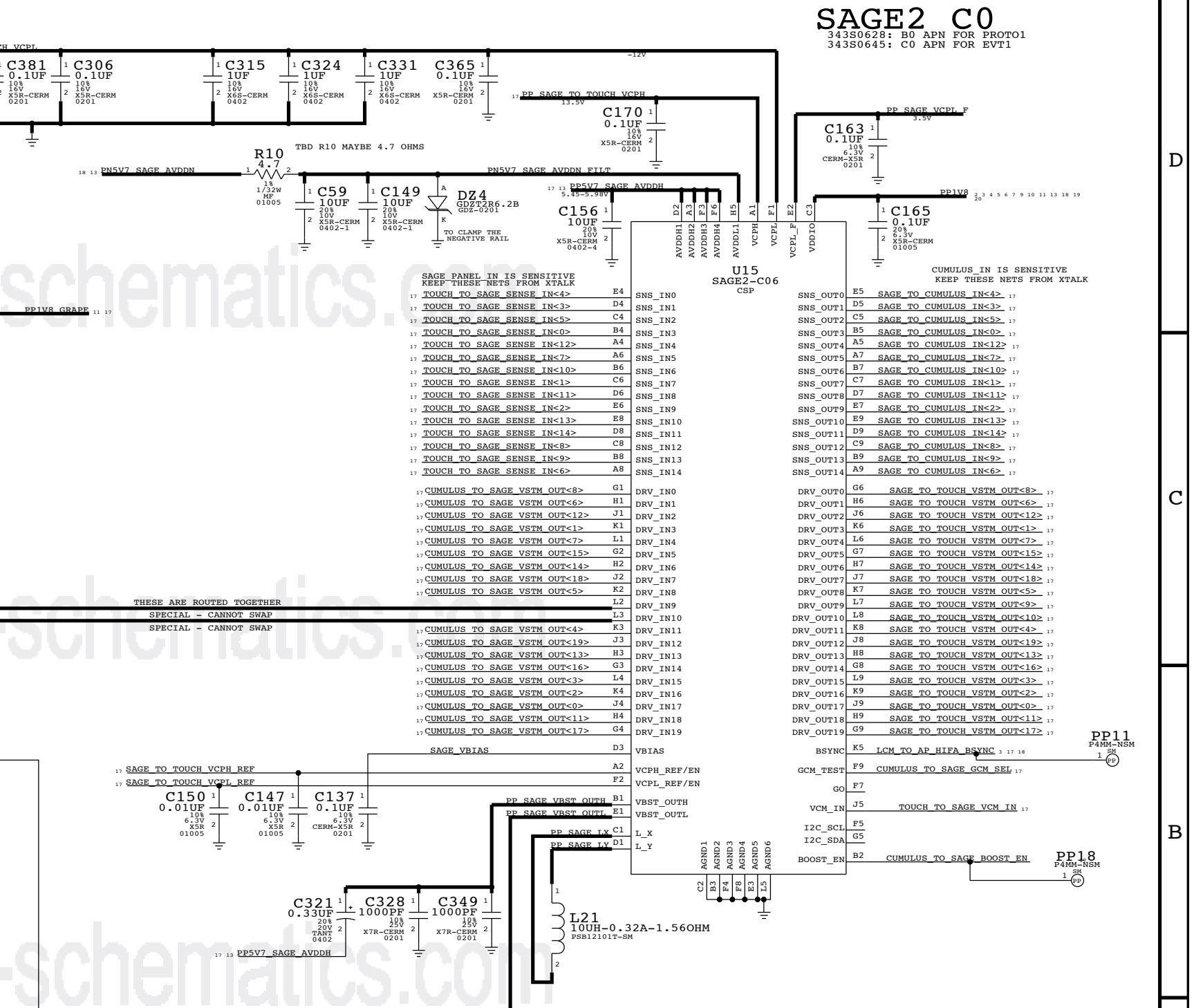
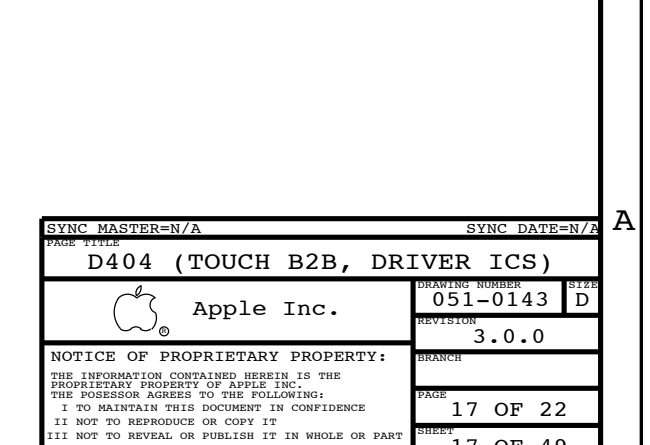
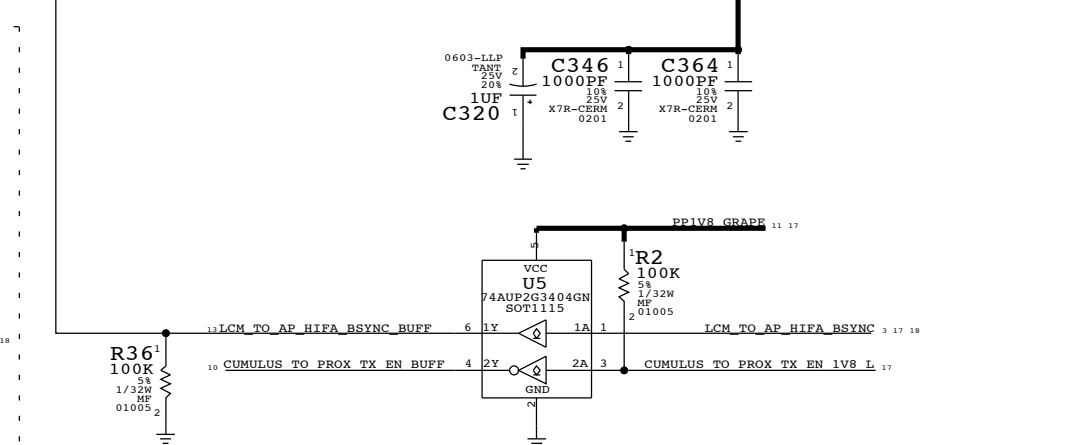
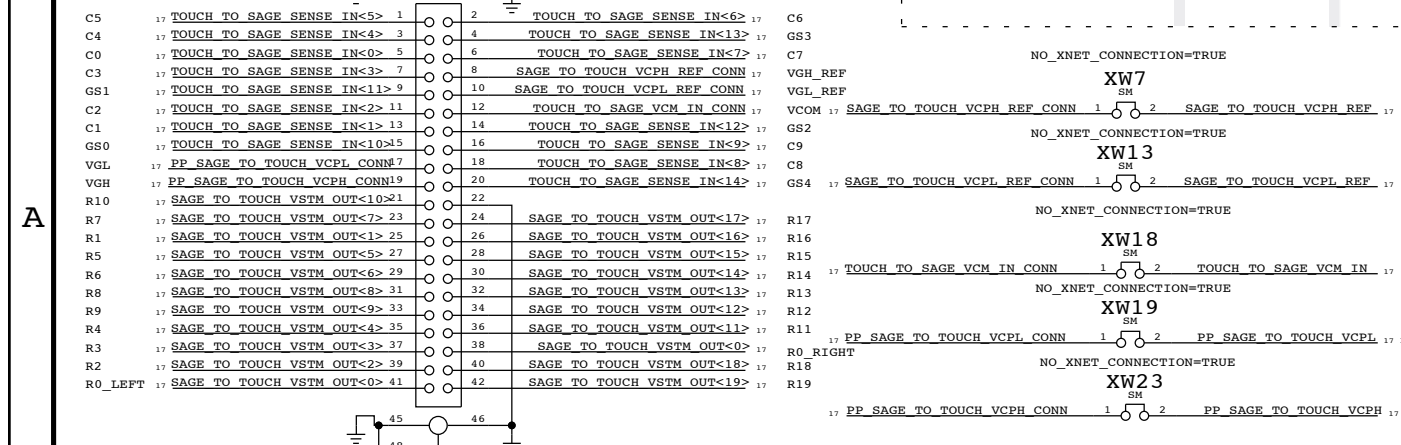
D404 (B2B, DRIVER ICS)

SAGE2 C0

343S0628: B0 APN FOR PROTO1
343S0645: C0 APN FOR EVT1

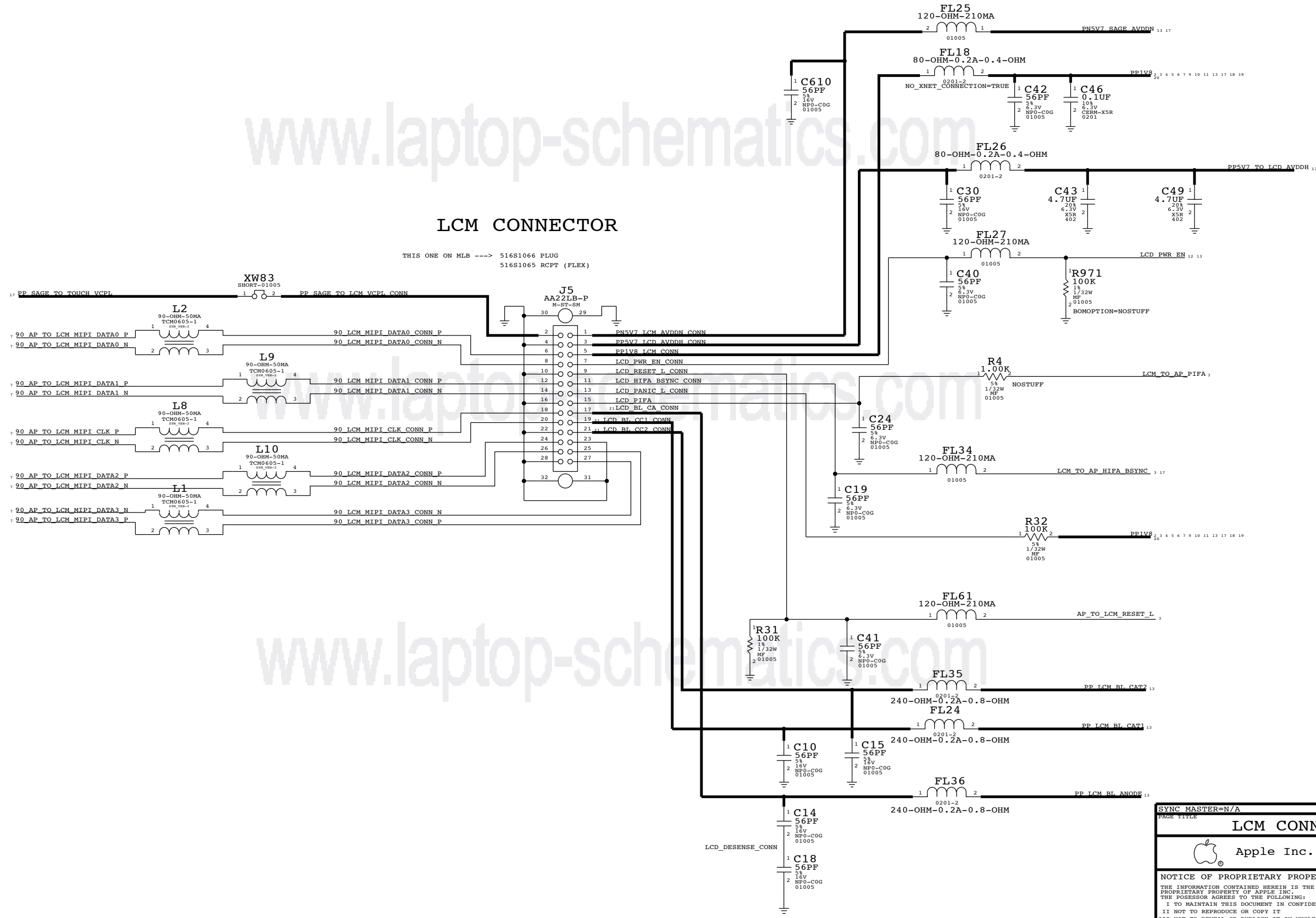


343S0638



LCM B2B

LCM CONNECTOR



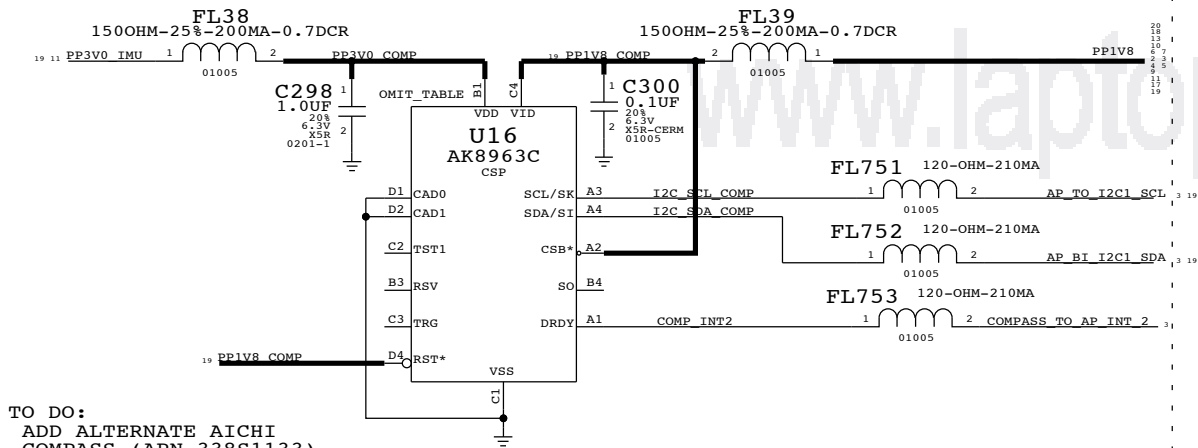
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LCM CONNECTOR			
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	REVISION	3.0.0	
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SENSORS

THIS PART OUTSIDE OF SHIELD

COMPASS

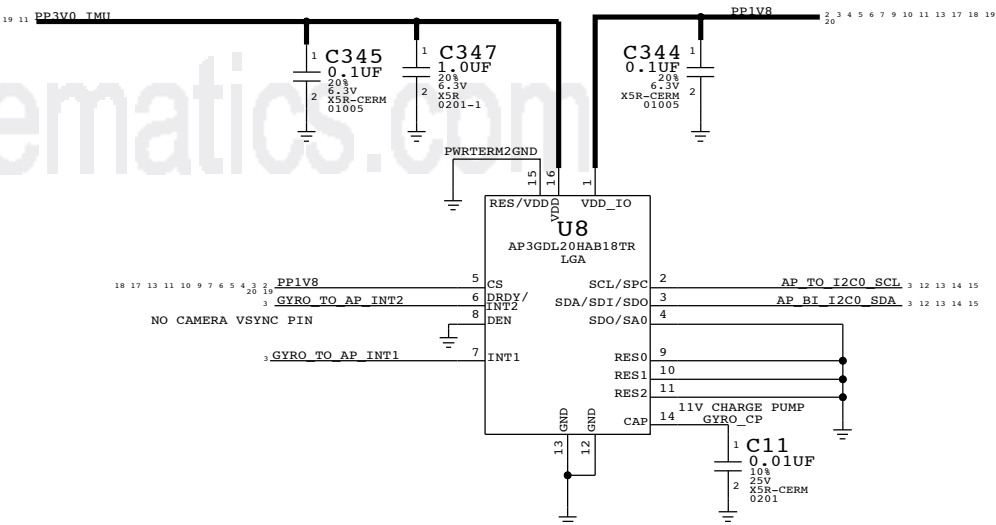
COMPASS DEVICE: 338S1014
COMPASS INTERPOSER: 998-5120



THESE PARTS INSIDE OF SHIELD

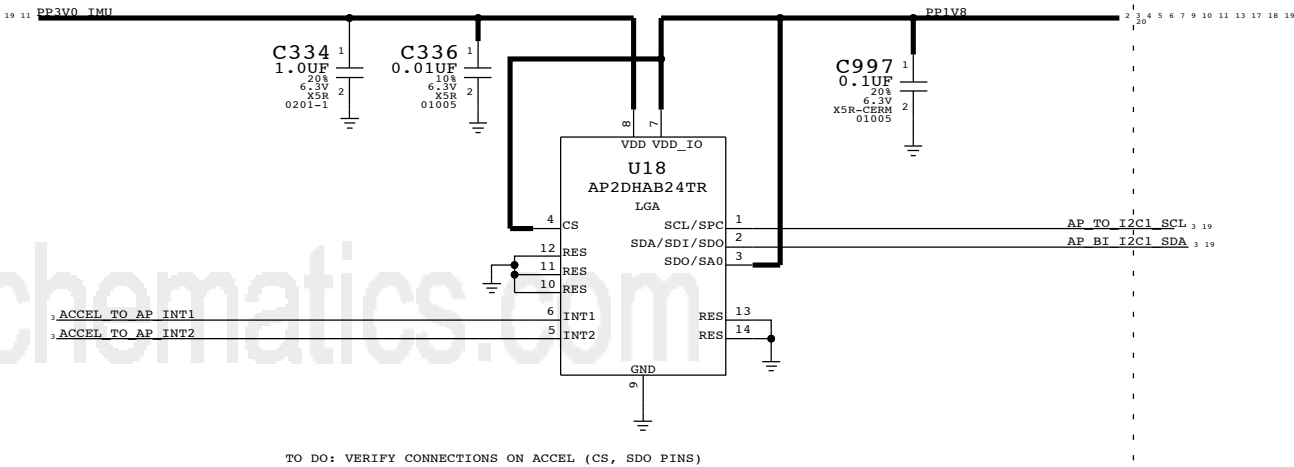
GYRO

AP3GDL20HAB, APN 338S1192



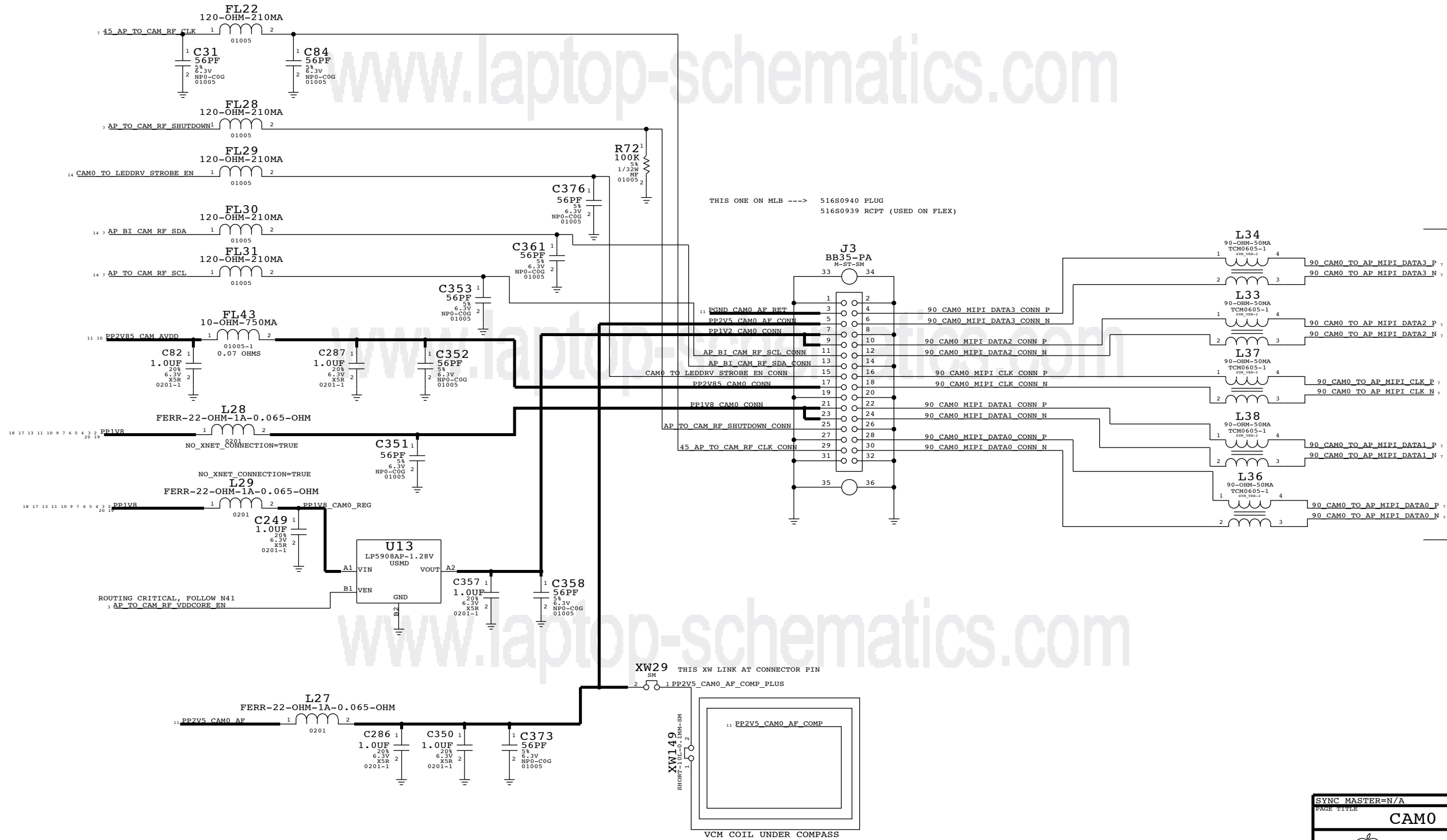
ACCELEROMETER

AP2DHAB, APN 338S1191



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OSCAR + SENSORS			
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CAM0: MAIN CAMERA CONNECTOR

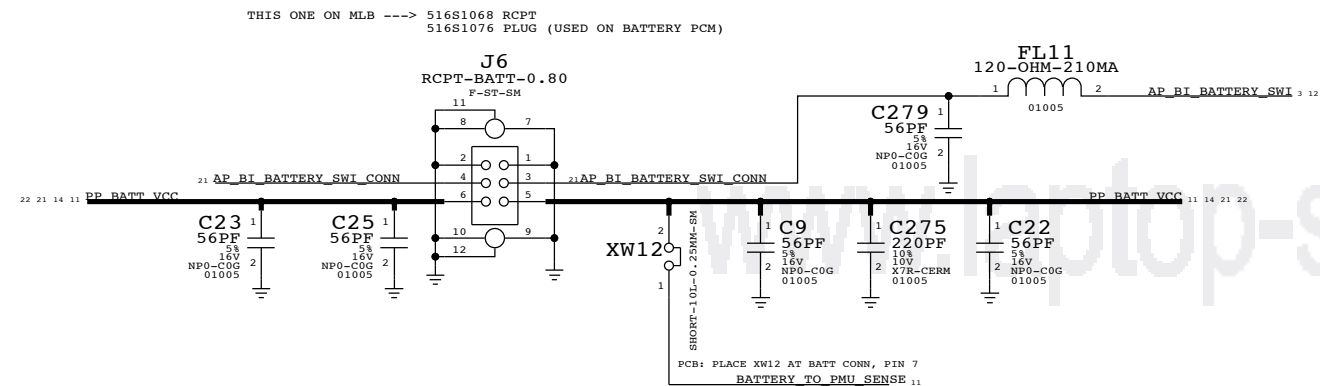


CAM0:
4-LANE MIPI

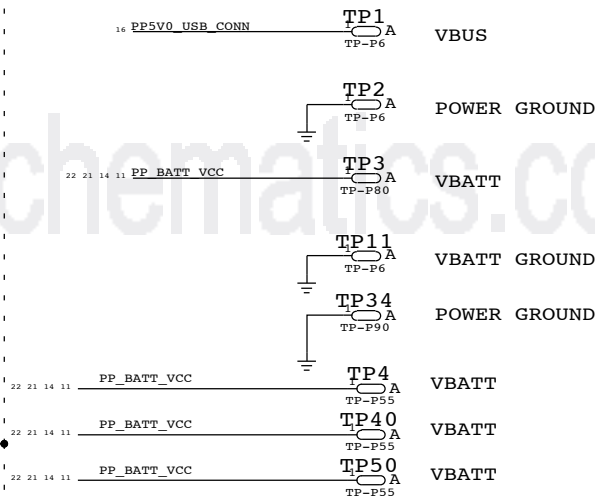
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CAMO CONNECTOR			
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		REVISION	
		3.0.0	
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BATT CONN, TPS, STANDOFFS/SHIELDS/FIDUCIALS

BATTERY CONN

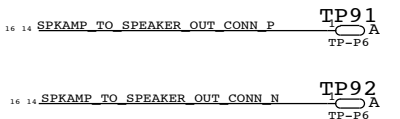


POWER TP

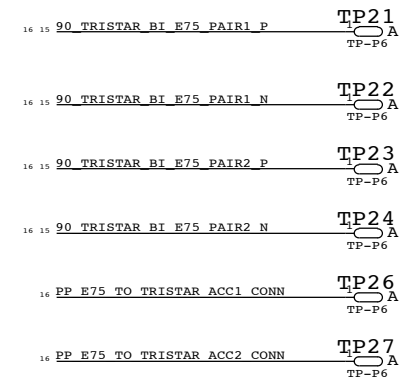


TESTPOINTS

SPKAMP OUTPUT TP



E75 - USB/UART/ID/POWER



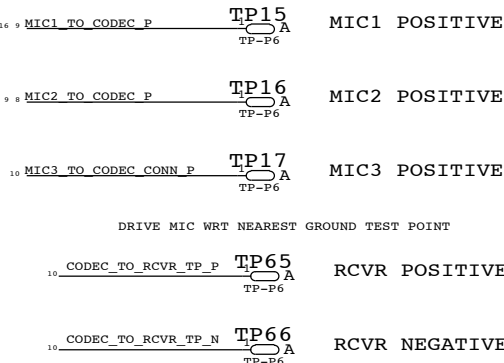
ACCESSORY ID AND POWER

POWER GROUND

POWER GROUND

FOR DIAGS

MIC AUDIO

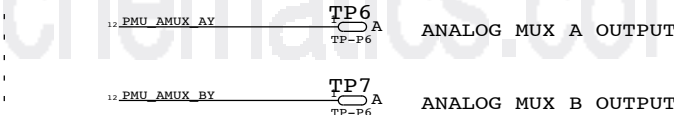


DRIVE MIC WRT NEAREST GROUND TEST POINT

RCVR POSITIVE

RCVR NEGATIVE

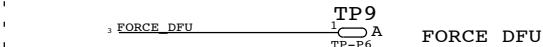
SUPER TP



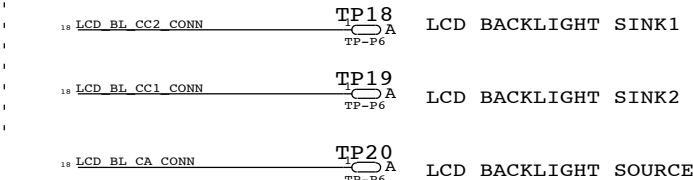
RESET



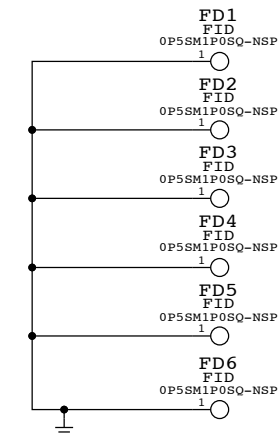
DFU



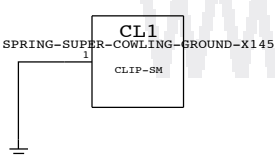
LCM BACKLIGHT



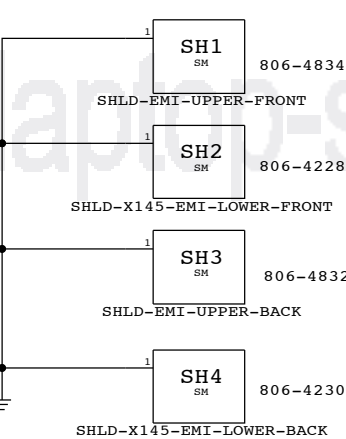
FIDUCIALS



COWLING UPPER LEFT

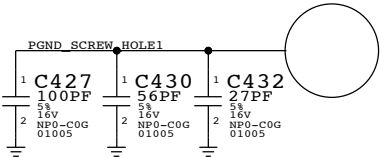


SHIELDS

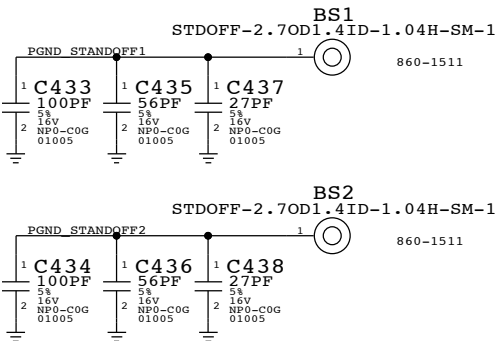


AC COUPLED SCREW HOLES + STANDOFFS (ON NORTH END OF SINGLE_BRD, TO MITIGATE COMPASS RETURN CURRENTS)

SCREW HOLES



STANDOFFS



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BATT B2B, TPS, PD FEATURES			
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RADIO_MLB HIERARCHICAL SYMBOL

AP/RADIO INTERFACE

SUBDESIGN_SUFFIX=RF I636			
24 21 14 11	IN	PP_BATT_VCC	MAKE_BASE=TRUE
45 13 12 11 9	IN	PP_VCC_MAIN	MAKE_BASE=TRUE
45 15 13 12 11 9 4 3	IN	PP1V8_SDRAM	MAKE_BASE=TRUE
25 14	OUT	PP_LDO14_2P65	MAKE_BASE=TRUE
24 3	IN	AP TO RADIO ON L	MAKE_BASE=TRUE
24 3	OUT	BB TO AP RESET DET L	MAKE_BASE=TRUE
24 12	IN	PMU TO BB_RST L	MAKE_BASE=TRUE
24 3	IN	AP TO BB_RST L	MAKE_BASE=TRUE
24 21 15 13 12 11 2	OUT	RESET 1V8 L	MAKE_BASE=TRUE
24 12	IN	45 PMU TO WLAN CLK32K	MAKE_BASE=TRUE
28 14	OUT	BB TO LEDDRV_GSM_BLANK	MAKE_BASE=TRUE
24 15	IN	90 TRISTAR_BI_BB_USB_N	MAKE_BASE=TRUE
24 15	IN	90 TRISTAR_BI_BB_USB_P	MAKE_BASE=TRUE
24 12	IN	PMU TO BB_VBUS_DET	MAKE_BASE=TRUE
24 3	IN	AP TO BB_UART1_RTS_L	MAKE_BASE=TRUE
24 3	OUT	BB TO AP_UART1_CTS_L	MAKE_BASE=TRUE
24 15 3	IN	AP TO BB_UART1_TXD	MAKE_BASE=TRUE
24 15 3	IN	BB TO AP_UART1_RXD	MAKE_BASE=TRUE
24 15 3	OUT	BB TO PMU_HOST_WAKE	MAKE_BASE=TRUE
24 12	OUT	BB TO AP_PP_SYNC	MAKE_BASE=TRUE
24 3	IN	45 BB TO AP_I2S1_BCLK	MAKE_BASE=TRUE
24 3	IN	AP TO BB_I2S1_DOUT	MAKE_BASE=TRUE
24 3	OUT	BB TO AP_I2S1_DIN	MAKE_BASE=TRUE
24 3	IN	BB TO AP_I2S1_LRCLK	MAKE_BASE=TRUE
24 12	OUT	RADIO TO PMU_ADC_SMPS1_MSMC_1V05	MAKE_BASE=TRUE
24 12	OUT	RADIO TO PMU_ADC_SMPS3_MSME_1V8	MAKE_BASE=TRUE
24 12	OUT	RADIO TO PMU_ADC_LDO6_RUIM_1V8	MAKE_BASE=TRUE
24 12	OUT	RADIO TO PMU_ADC_LVS1	MAKE_BASE=TRUE
24 12	IN	PMU TO WLAN_REG_ON	MAKE_BASE=TRUE
45 3	IN	AP TO WLAN_UART4_TXD	MAKE_BASE=TRUE
45 3	OUT	WLAN TO AP_UART4_RXD	MAKE_BASE=TRUE
45 12	OUT	WLAN TO PMU_HOST_WAKE	MAKE_BASE=TRUE
24 12	IN	PMU TO BT_REG_ON	MAKE_BASE=TRUE
45 3	IN	AP TO BT_UART3_RTS_L	MAKE_BASE=TRUE
45 3	OUT	BT TO AP_UART3_CTS_L	MAKE_BASE=TRUE
24 3	IN	AP TO BT_UART3_TXD	MAKE_BASE=TRUE
24 3	OUT	BT TO AP_UART3_RXD	MAKE_BASE=TRUE
24 3	IN	AP TO BT_WAKE	MAKE_BASE=TRUE
45 12	OUT	BT TO PMU_HOST_WAKE	MAKE_BASE=TRUE
45 3	IN	45 AP TO BT_I2S3_BCLK	MAKE_BASE=TRUE
45 3	IN	AP TO BT_I2S3_DOUT	MAKE_BASE=TRUE
45 3	IN	BT TO AP_I2S3_DIN	MAKE_BASE=TRUE
45 3	IN	AP TO BT_I2S3_LRCLK	MAKE_BASE=TRUE
24 2	IN	50 AP_BI_BB_HSI1_DATA	MAKE_BASE=TRUE
24 2	IN	50 AP_BI_BB_HSI1_STB	MAKE_BASE=TRUE
24 3	IN	AP TO BB_HSI1_RDY	MAKE_BASE=TRUE
24 3	OUT	BB TO AP_HSI1_RDY	MAKE_BASE=TRUE
28 3	IN	BB TO AP_HSI1_REMOTE_WAKE	MAKE_BASE=TRUE
28 3	IN	AP TO BB_WAKE_MODEM	MAKE_BASE=TRUE
24 2	IN	50 AP_BI_WLAN_HSI3_DATA	MAKE_BASE=TRUE
24 2	IN	50 AP_BI_WLAN_HSI3_STB	MAKE_BASE=TRUE
24 3	OUT	AP TO WLAN_HSI2_RDY	MAKE_BASE=TRUE
24 3	OUT	WLAN TO AP_HSI2_RDY	MAKE_BASE=TRUE
24 3	IN	WLAN TO AP_HSI2_REMOTE_WAKE	MAKE_BASE=TRUE
24 16	OUT	BB TO LAT_SW1_CTL	MAKE_BASE=TRUE
28 16	OUT	BB TO LAT_SW2_CTL	MAKE_BASE=TRUE
28	OUT	BB TO ANTENNA_PAC_SPI_CS_L	MAKE_BASE=TRUE
28	OUT	BB TO ANTENNA_PAC_SPI_SCL	MAKE_BASE=TRUE
28	OUT	BB TO ANTENNA_PAC_SPI_MOSI	MAKE_BASE=TRUE
28	IN	ANTENNA_PAC TO BB_SPI_MISO	MAKE_BASE=TRUE
28 3	IN	BB TO AP_IPC_GPIO	MAKE_BASE=TRUE
PP_BATT_VCC_CONN			
PP_VCC_MAIN_WLAN			
PP_WL_BT_VDDIO_AP			
PP_LDO14_2V65			
RADIO_ON_L		BB_JTAG_TCK	MAKE_BASE=TRUE
RESET_DET_L		BB_JTAG_TDI	MAKE_BASE=TRUE
RESET_PMU_L		BB_JTAG_TMS	MAKE_BASE=TRUE
BB_RST_L		BB_JTAG_TRST_L	MAKE_BASE=TRUE
RF_RESET_L		BB_JTAG_TDO	MAKE_BASE=TRUE
CLK32K_AP			
TX_GTR_THRESH			
90_BB_USB_D_N			
90_BB_USB_D_P			
BB_USB_VBUS			
BB_UART_CTS_L			
BB_UART_RTS_L			
BB_UART_RXD			
BB_UART_TXD			
HOST_WAKE_BB			
PP_SYNC			
BB_I2S_CLKRADIO_MLB			
BB_I2S_RXD			
BB_I2S_TXD			
BB_I2S_WS			
ADC_SMPS1_MSMC_1V05			
ADC_SMPS3_MSME_1V8			
ADC_LDO6_RUIM_1V8			
ADC_LVS1			
WLAN_REG_ON			
WLAN_UART_RXD			
WLAN_UART_TXD			
HOST_WAKE_WLAN			
BT_REG_ON			
BT_UART_CTS_L			
BT_UART_RTS_L			
BT_UART_RXD			
BT_UART_TXD			
BT_WAKE			
HOST_WAKE_BT			
BT_PCM_CLK			
BT_PCM_IN			
BT_PCM_OUT			
BT_PCM_SYNC			
50_HSI1_BB_DATA			
50_HSI1_BB_STROBE			
AP_HSI1_RDY			
PBL_RUN_BB_HSI1_RDY			
BB_HSI1_REMOTE_WAKE			
AP_WAKE_MODEM			
50_HSI1_WLAN_DATA			
50_HSI1_WLAN_STROBE			
AP_HSI3_RDY			
WLAN_HSI3_DEVICE_RDY			
WLAN_HSI3_RESUME			
LAT_SW1_CTL			
LAT_SW2_CTL			
BB_SPI_TO_PAC_CS			
BB_SPI_TO_PAC_CLK			
BB_SPI_TO_PAC_DATA_MOSI			
PAC_TO_BB_SPI_DATA_MISO			
BB_IPC_GPIO			
OSCAR_CONTEXT_A			
OSCAR_CONTEXT_B			

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D

C

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C

B

A

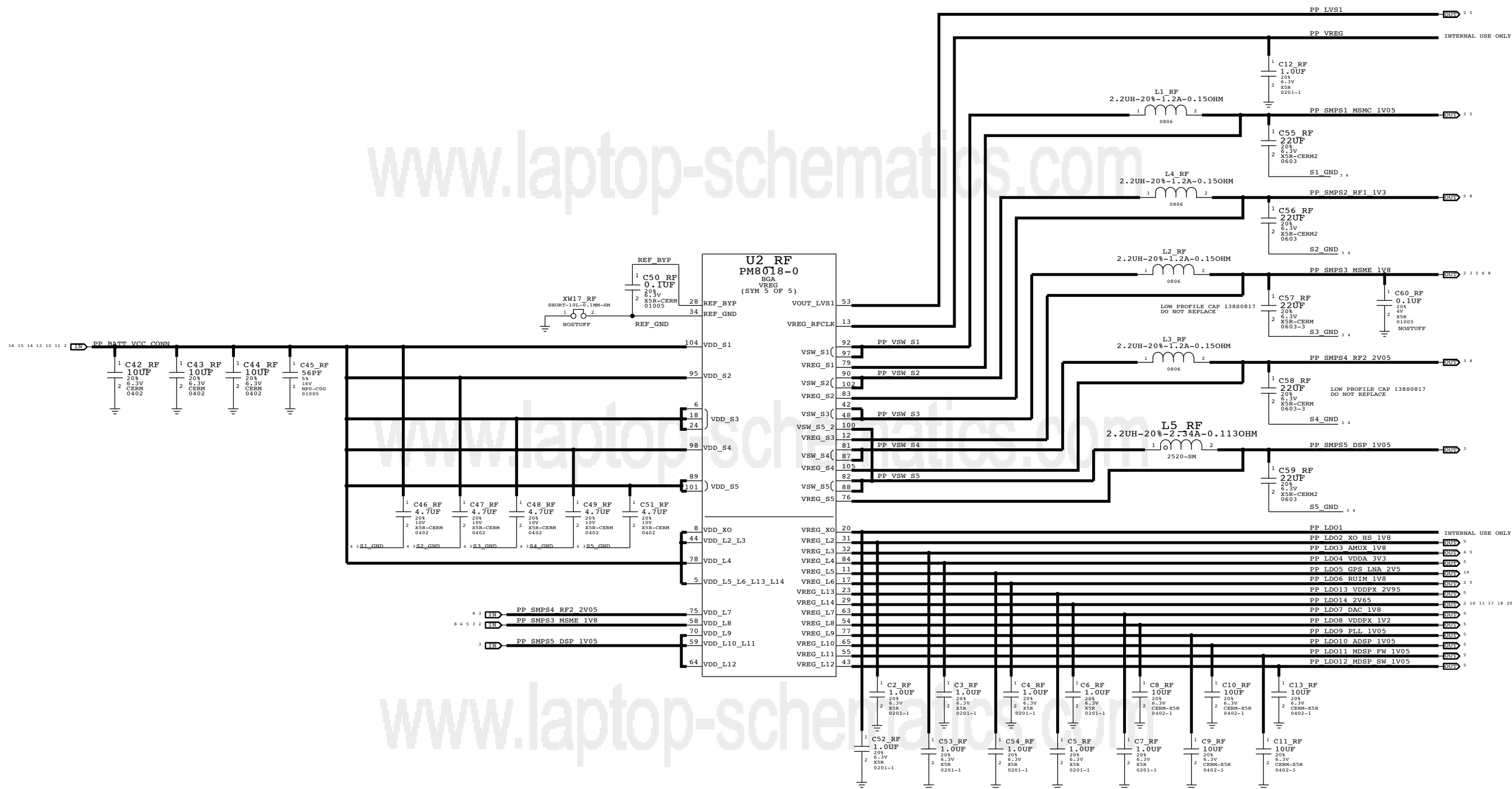
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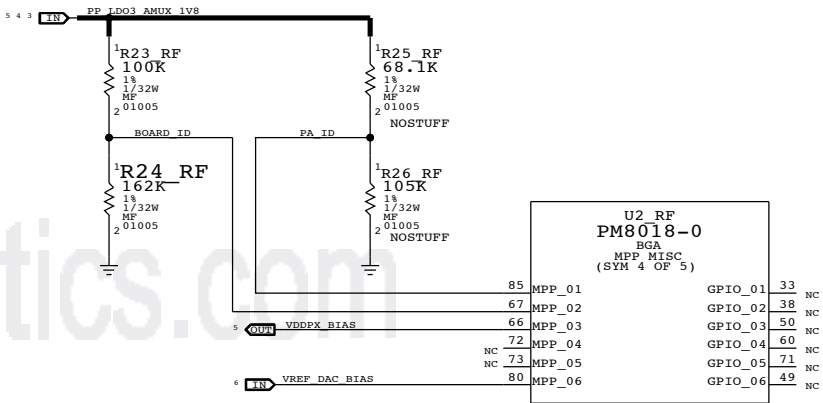


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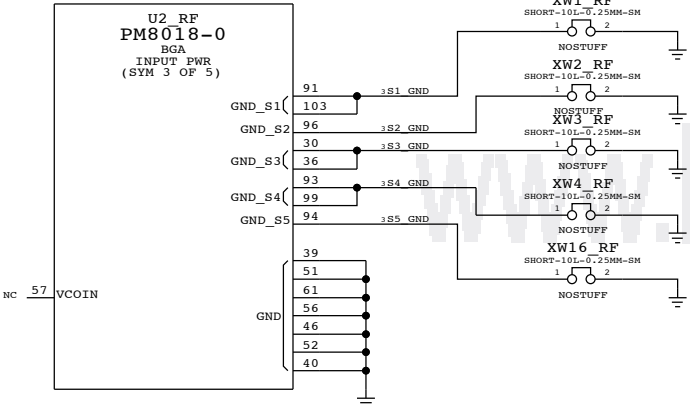
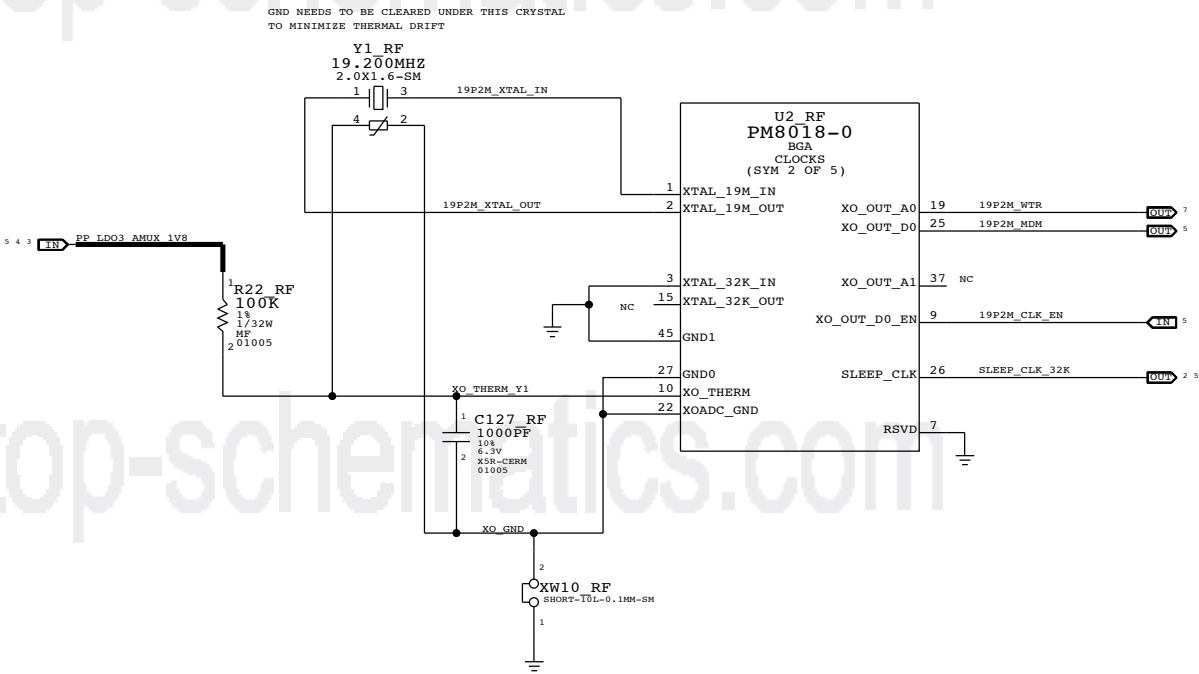
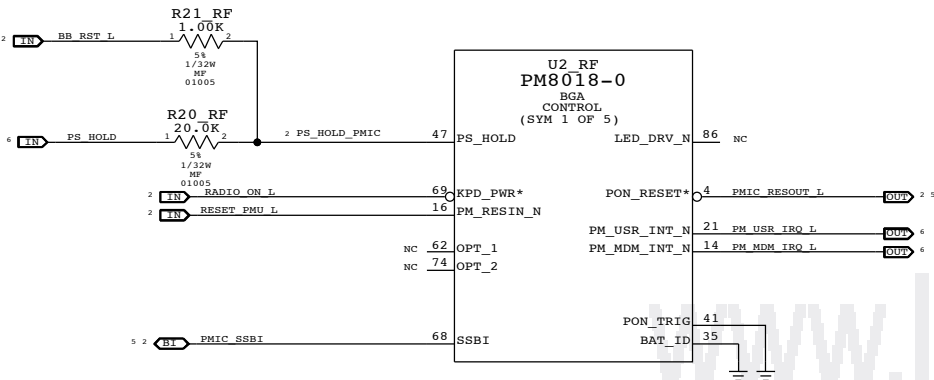
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PA_ID	CONFIG
1.1V	MAV7 APAC
1.3V	MAV7 EU
1.5V	MAV7.3 APAC
1.7V	MAV7.3 EU

BOARD_ID	REVISION
0.1V	UNUSED
0.3V	UNUSED
0.5V	UNUSED
0.7V	PROTO1
0.9V	PROTO2
1.1V	EVT1
1.3V	E1C
1.5V	EVT2
1.7V	DVT/PVT

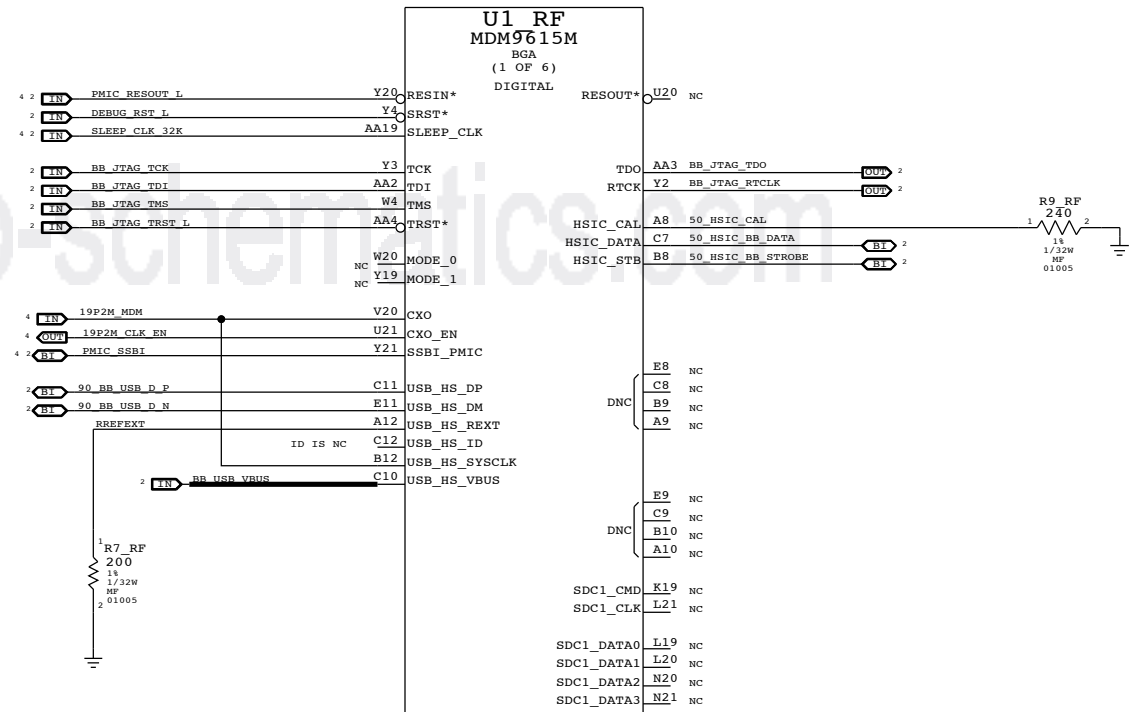
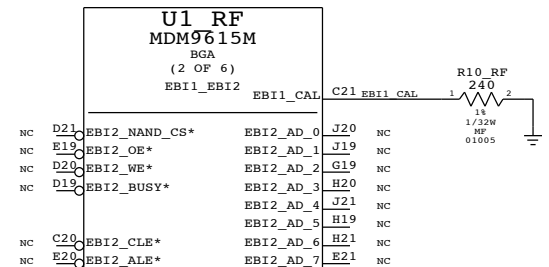
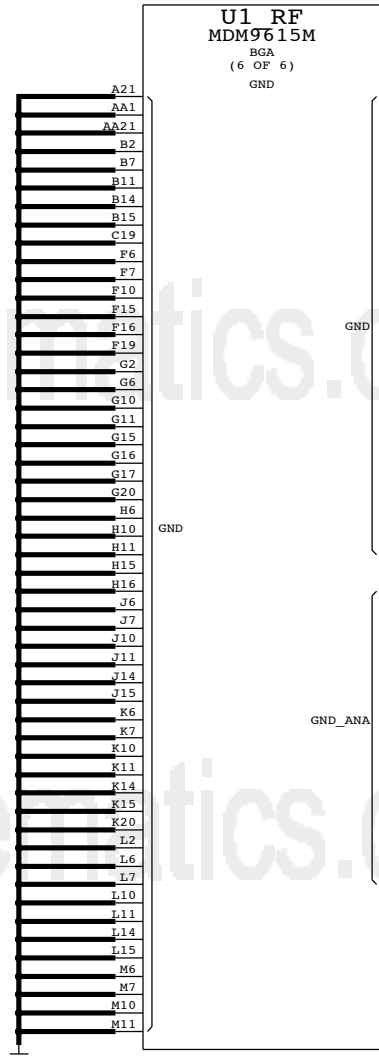
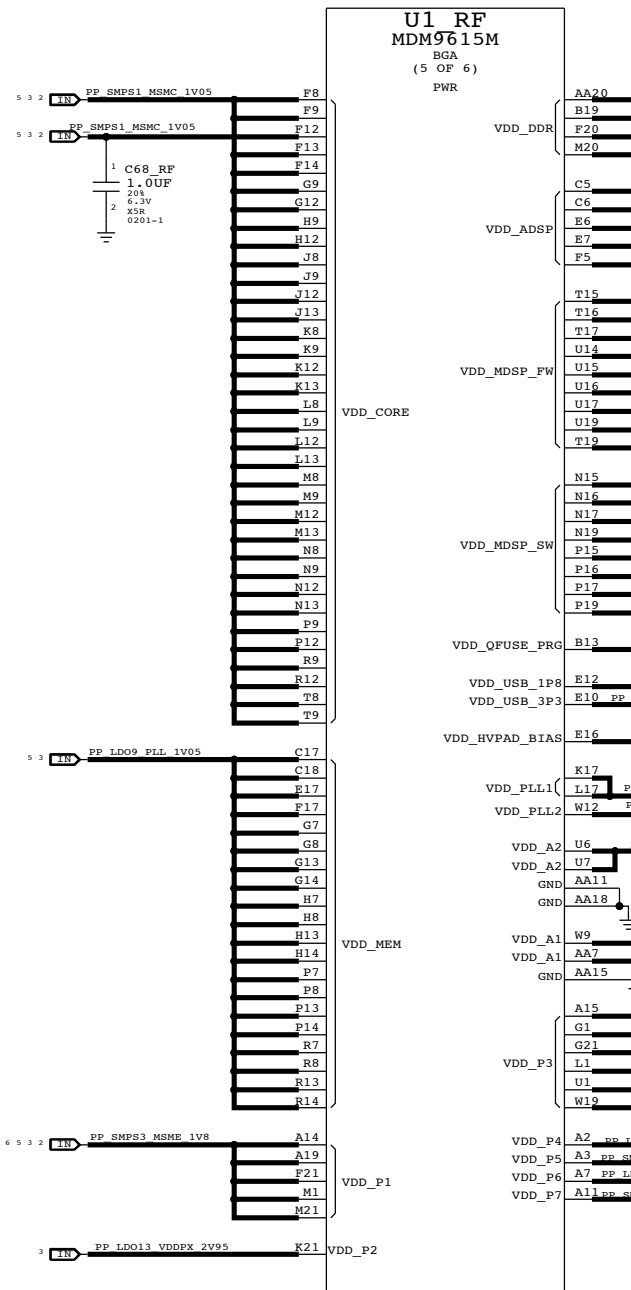
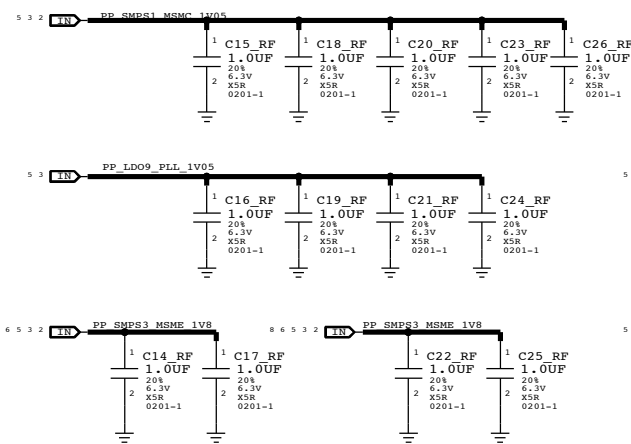


AP SECTION NEEDS ITS OWN THERMISTOR PLACED NEAR THE PA'S.



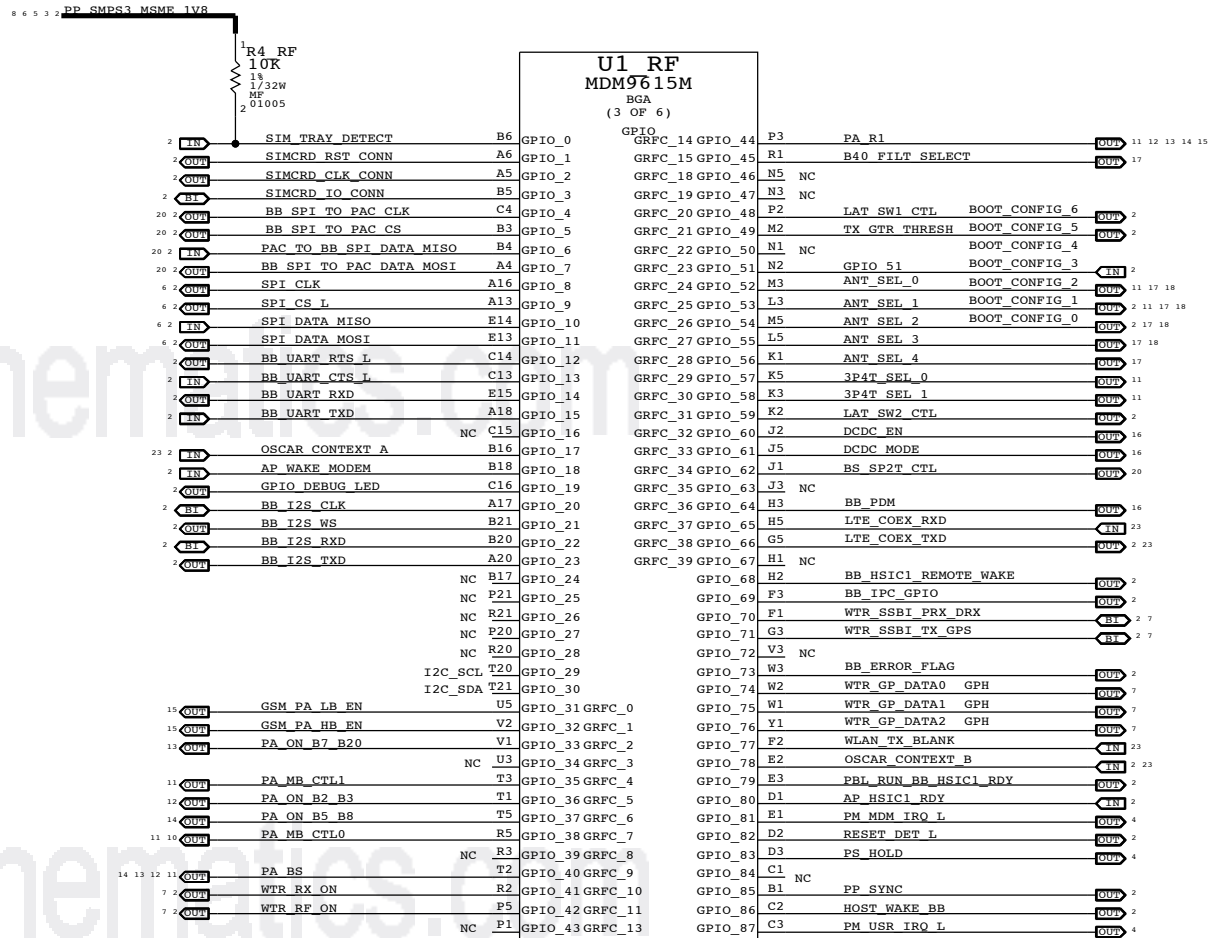
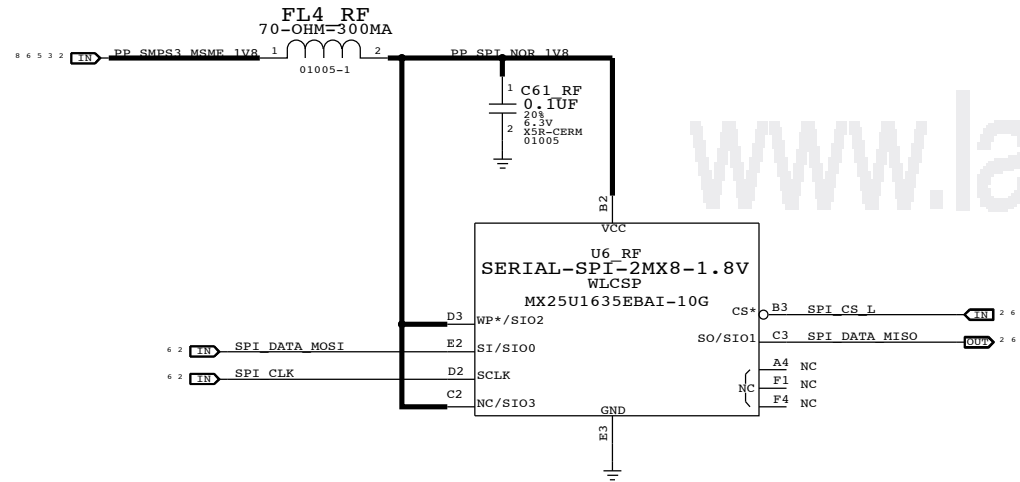
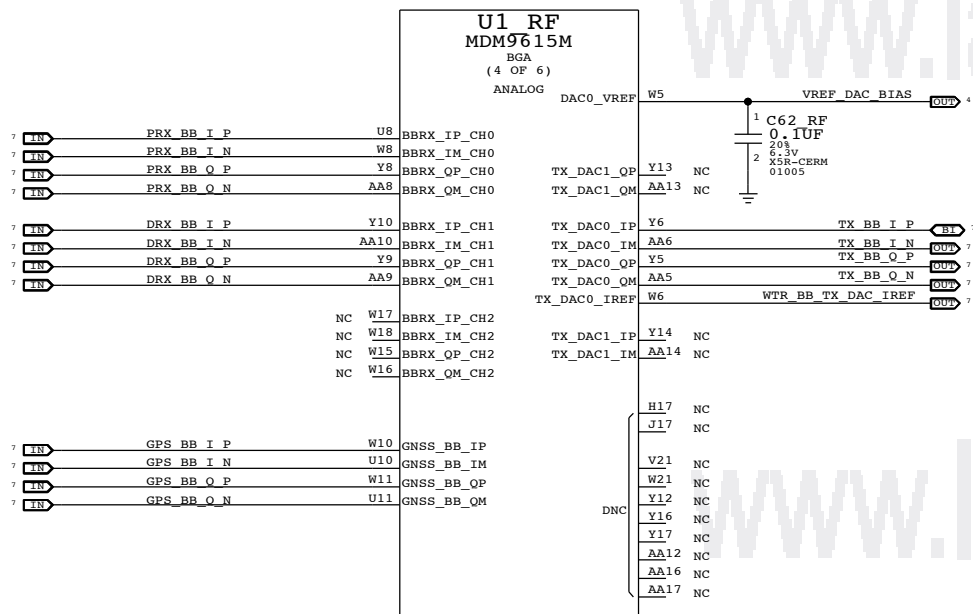
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BASEBAND (1 OF 2)



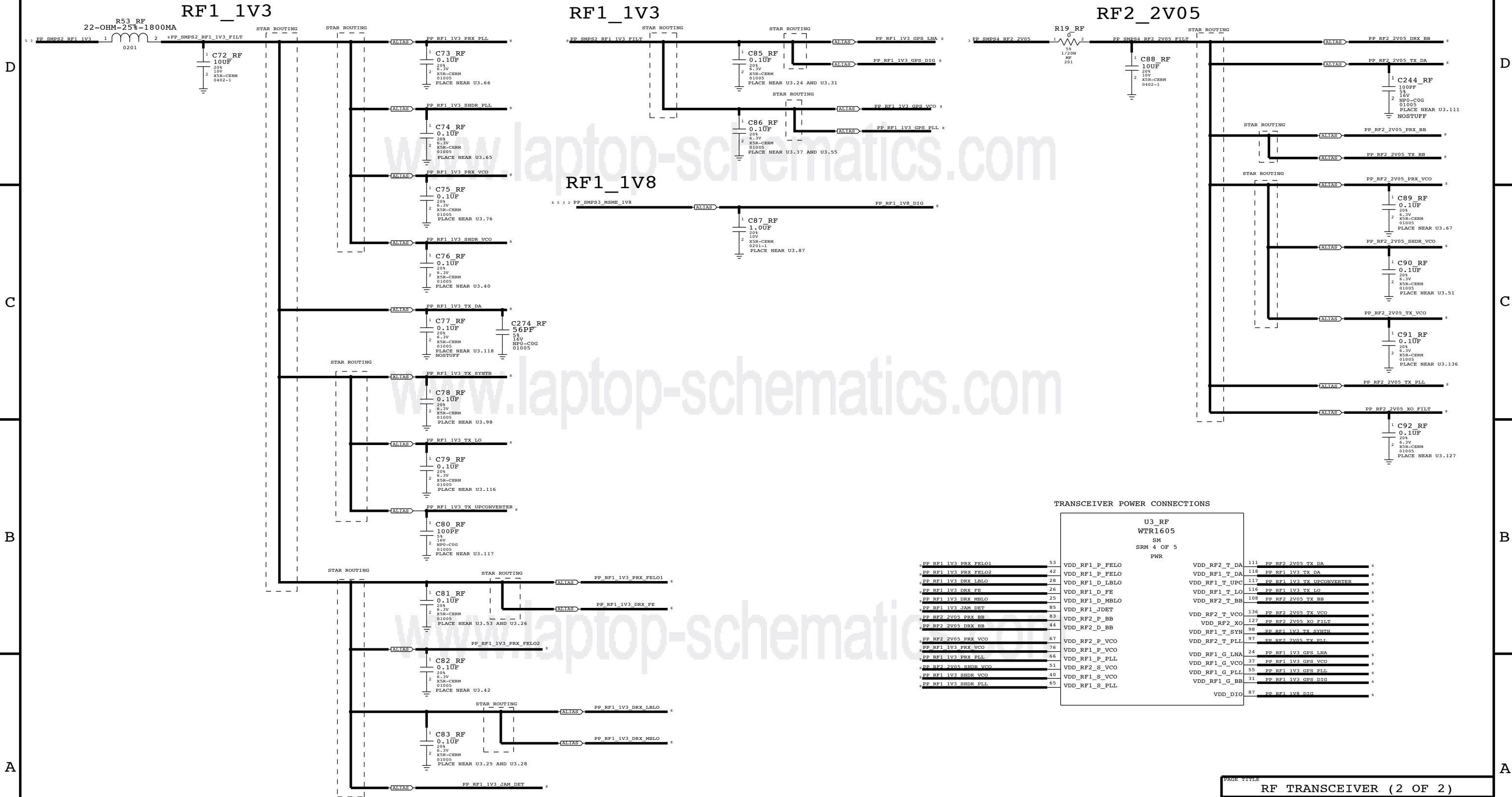
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BASEBAND (2 OF 2)



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BRANCH	PAGE 6 OF 27
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RF TRANSCEIVER (2 OF 2)

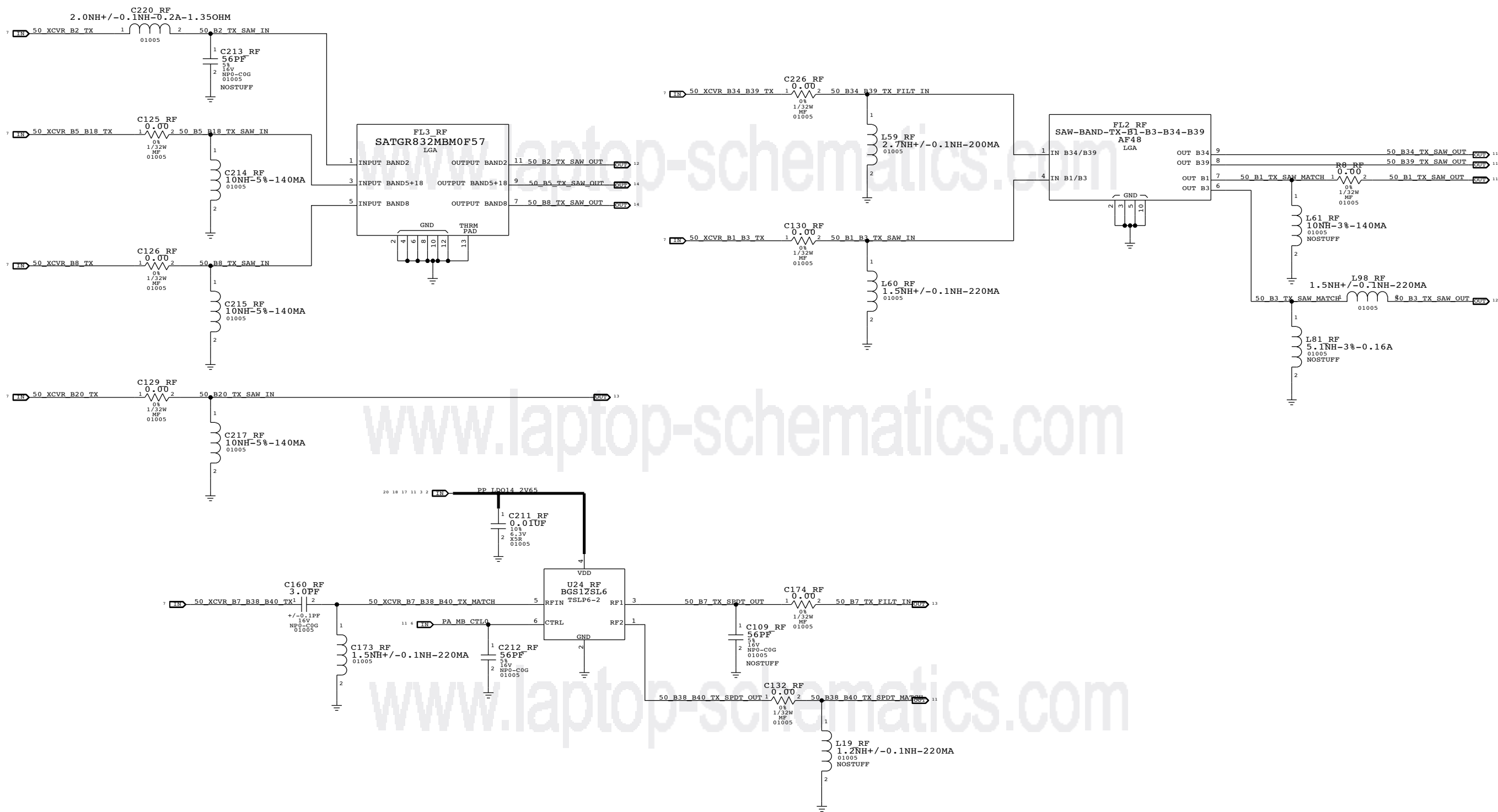


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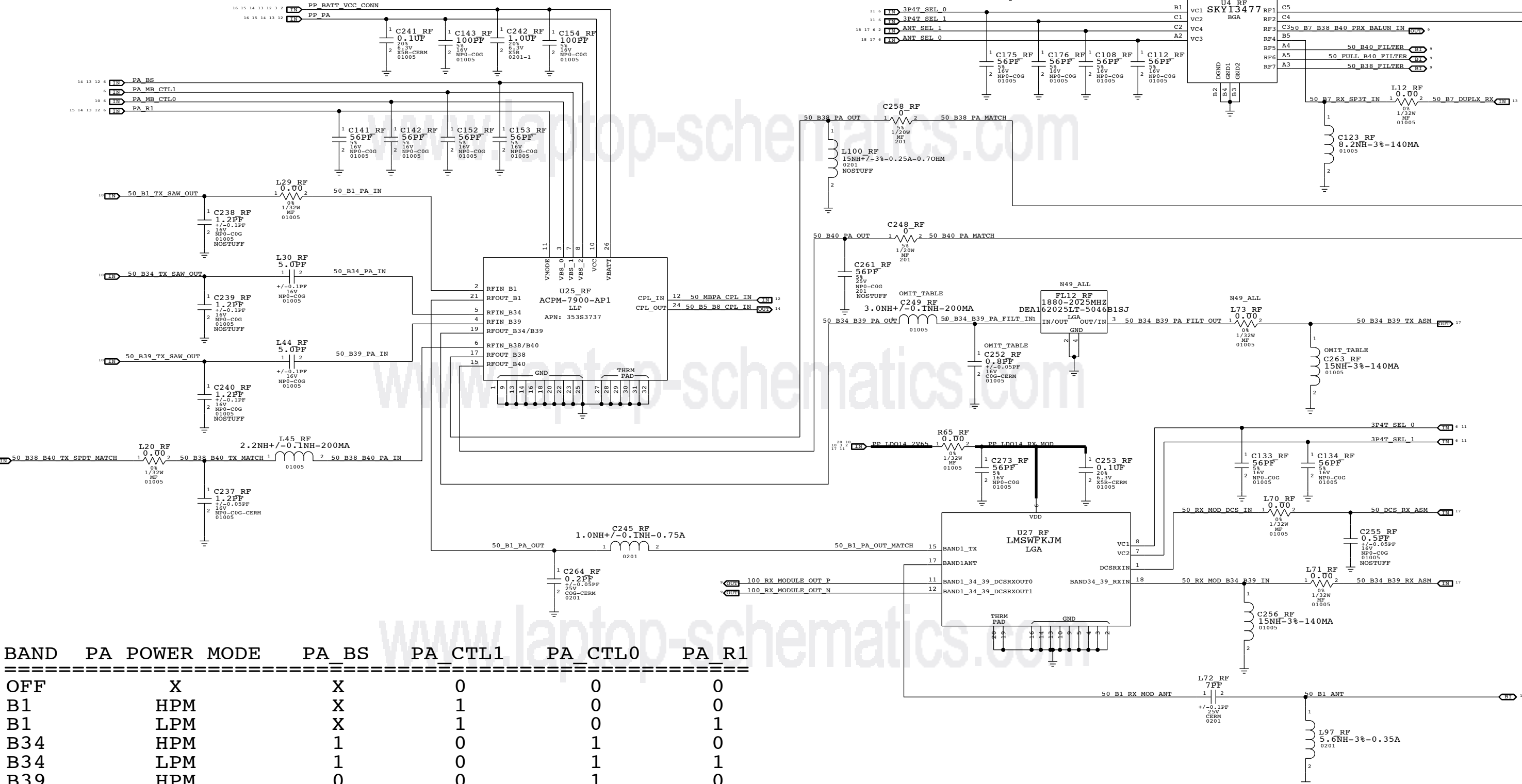
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TX INTERSTAGE FILTERS



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	PAGE	10 OF 27
	SHEET	32 OF 49

BAND 1/34/39/38/40 TX



BAND	PA	POWER	MODE	PA_BS	PA_CTL1	PA_CTL0	PA_R1
OFF		X		X	0	0	0
B1		HPM		X	1	0	0
B1		LPM		X	1	0	1
B34		HPM		1	0	1	0
B34		LPM		1	0	1	1
B39		HPM		0	0	1	0
B39		LPM		0	0	1	1
B38		HPM		1	1	1	0
B38		LPM		1	1	1	1
B40		HPM		0	1	1	0
B40		LPM		0	1	1	1

PENTABAND PA

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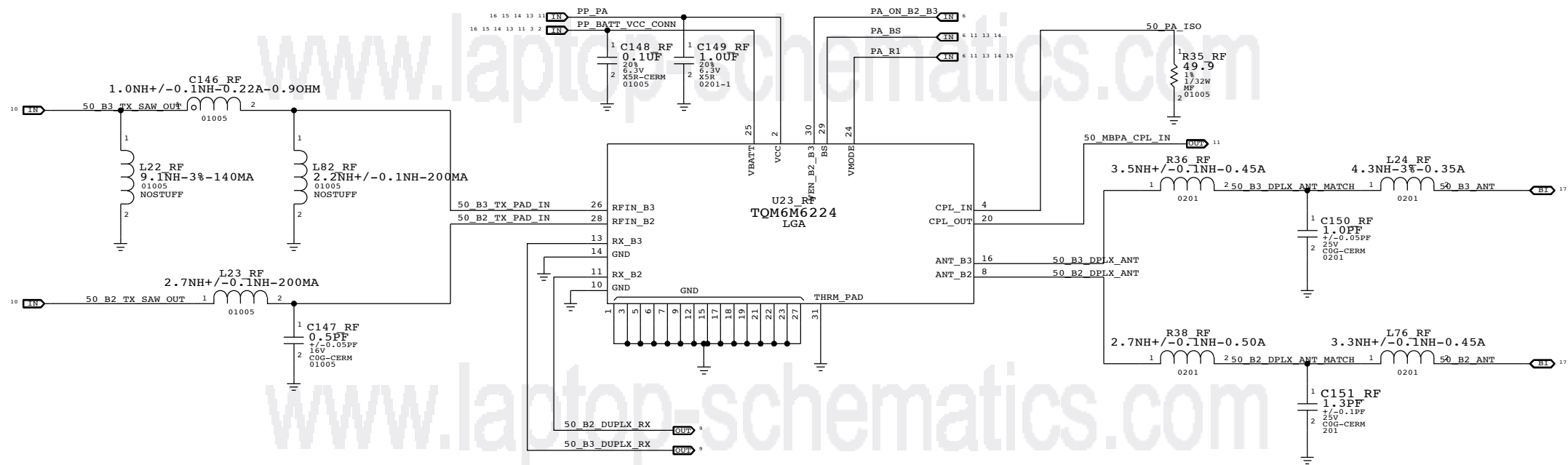
BRANCH

PAGE
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SHEET
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SIZE
D

BAND 2/3 PAD



BAND	PA	POWER	MODE	PA	BS	PA	ON	B2	B3	PA	R1
=====	=====	=====	=====	=====	=====	=====	=====	=====	=====	=====	=====
OFF		X		X		0				X	
B3		HPM		0		1				0	
B3		LPM		0		1				1	
B2		HPM		1		1				0	
B2		LPM		1		1				1	

BAND 2/3 PAD

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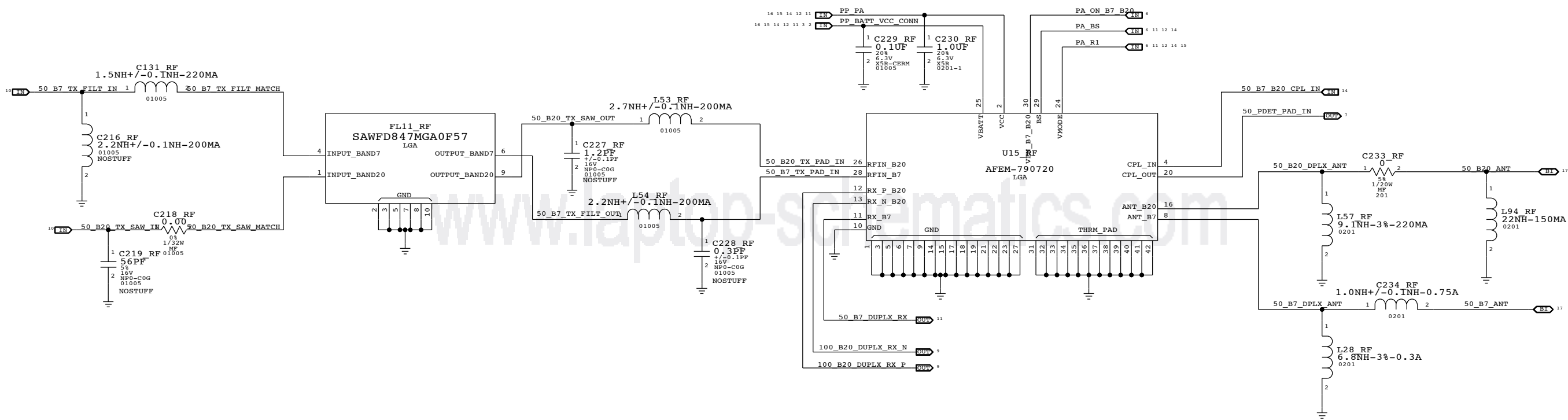
12 OF 27

SHEET

34 OF 49

BAND 20/7 PAD

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BAND	PA POWER MODE	PA BS	PA ON B20 B7	PA R1
OFF	X	X	0	X
B20	HPM	0	1	0
B20	LPM	0	1	1
B7	HPM	1	1	0
B7	LPM	1	1	1

BAND 7/20 PAD

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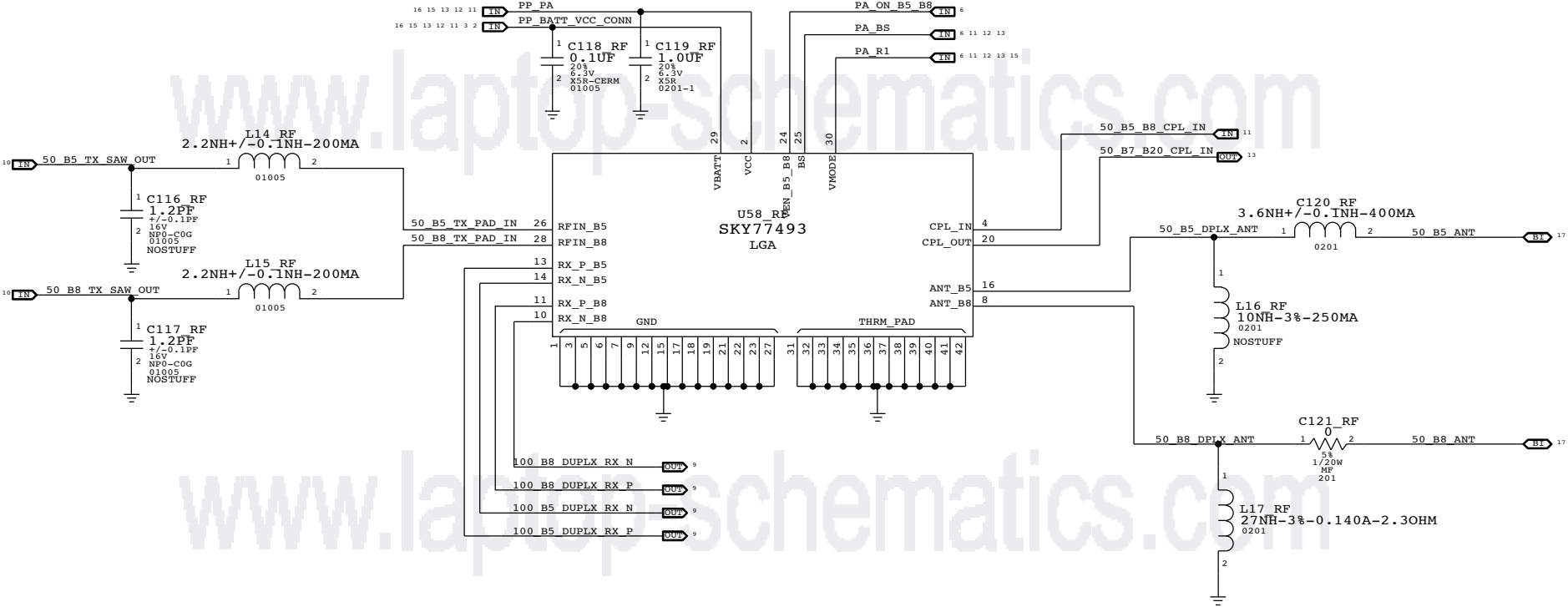
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BAND 5/8 PAD



BAND	PA POWER MODE	PA_BS	PA_ON_B5_B8	PA_R1
=====	=====	=====	=====	=====
OFF	X	X	0	X
B5	HPM	0	1	0
B5	LPM	0	1	1
B8	HPM	1	1	0
B8	LPM	1	1	1

BAND 5/8 PAD

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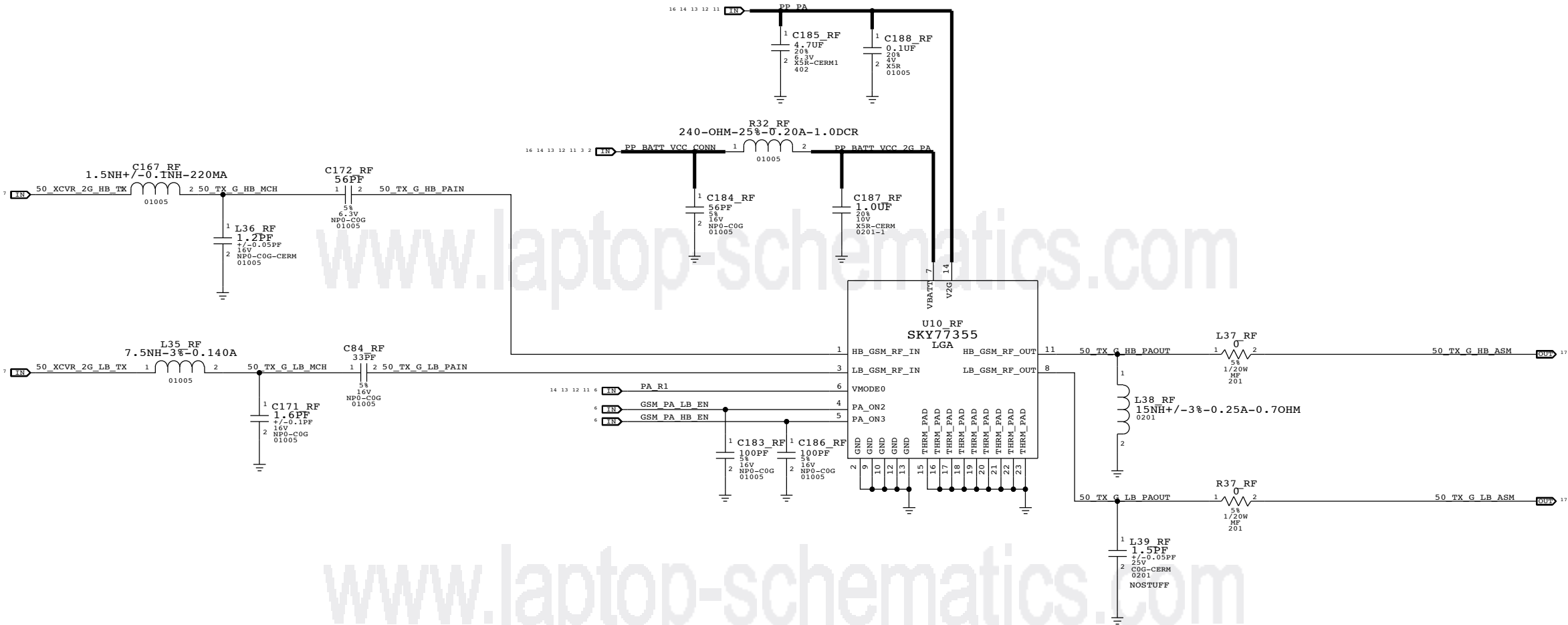
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2G PA

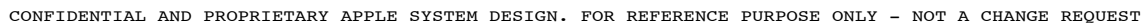
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2G PA GAIN MODES

BAND	MODE	GAIN MODE	PA_R1	PCL RANGE
LOW BAND	GSM	ULTRA LOW	HIGH	16 TO 19
LOW BAND	GSM	LOW	HIGH	14 TO 15
LOW BAND	GSM	MEDIUM	LOW	7 TO 13
LOW BAND	GSM	HIGH	LOW	5 TO 6
HIGH BAND	GSM	ULTRA LOW	HIGH	10 TO 15
HIGH BAND	GSM	LOW	HIGH	7 TO 9
HIGH BAND	GSM	HIGH	LOW	0 TO 6
LOW BAND	EDGE	LOW	HIGH	15 TO 19
LOW BAND	EDGE	MEDIUM	LOW	10 TO 14
LOW BAND	EDGE	HIGH	LOW	8 TO 9
HIGH BAND	EDGE	LOW	HIGH	9 TO 15
HIGH BAND	EDGE	HIGH	LOW	2 TO 8

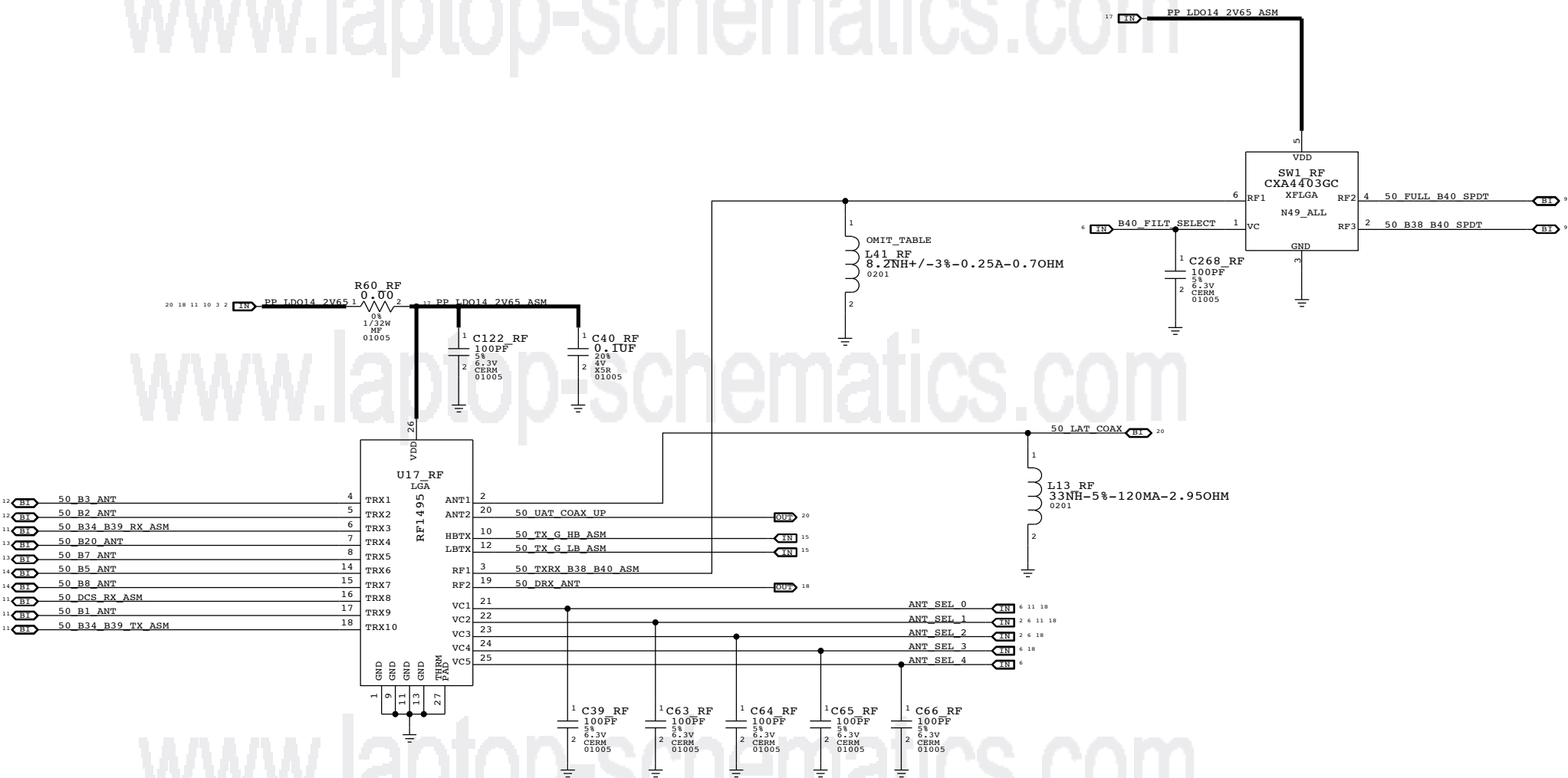


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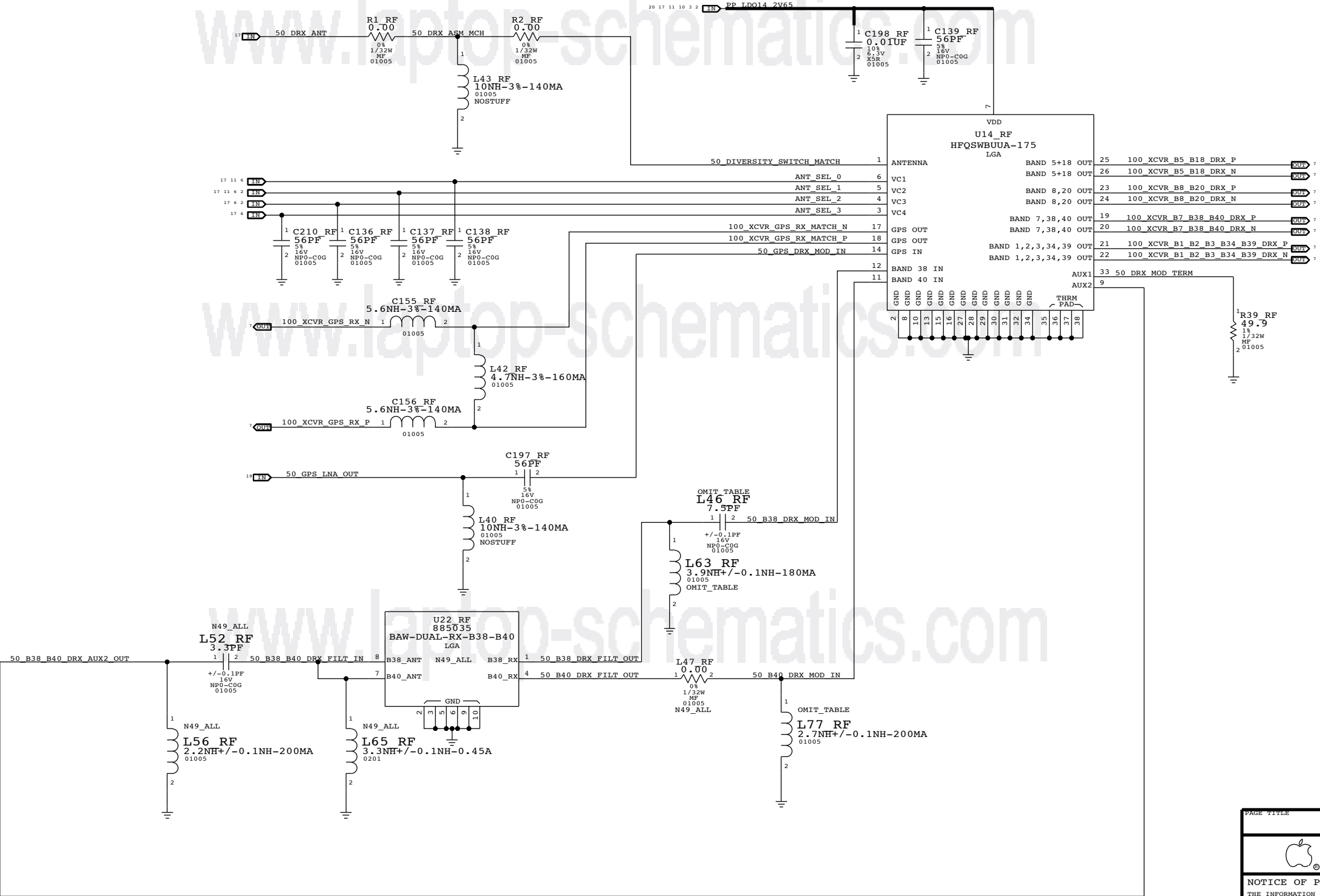
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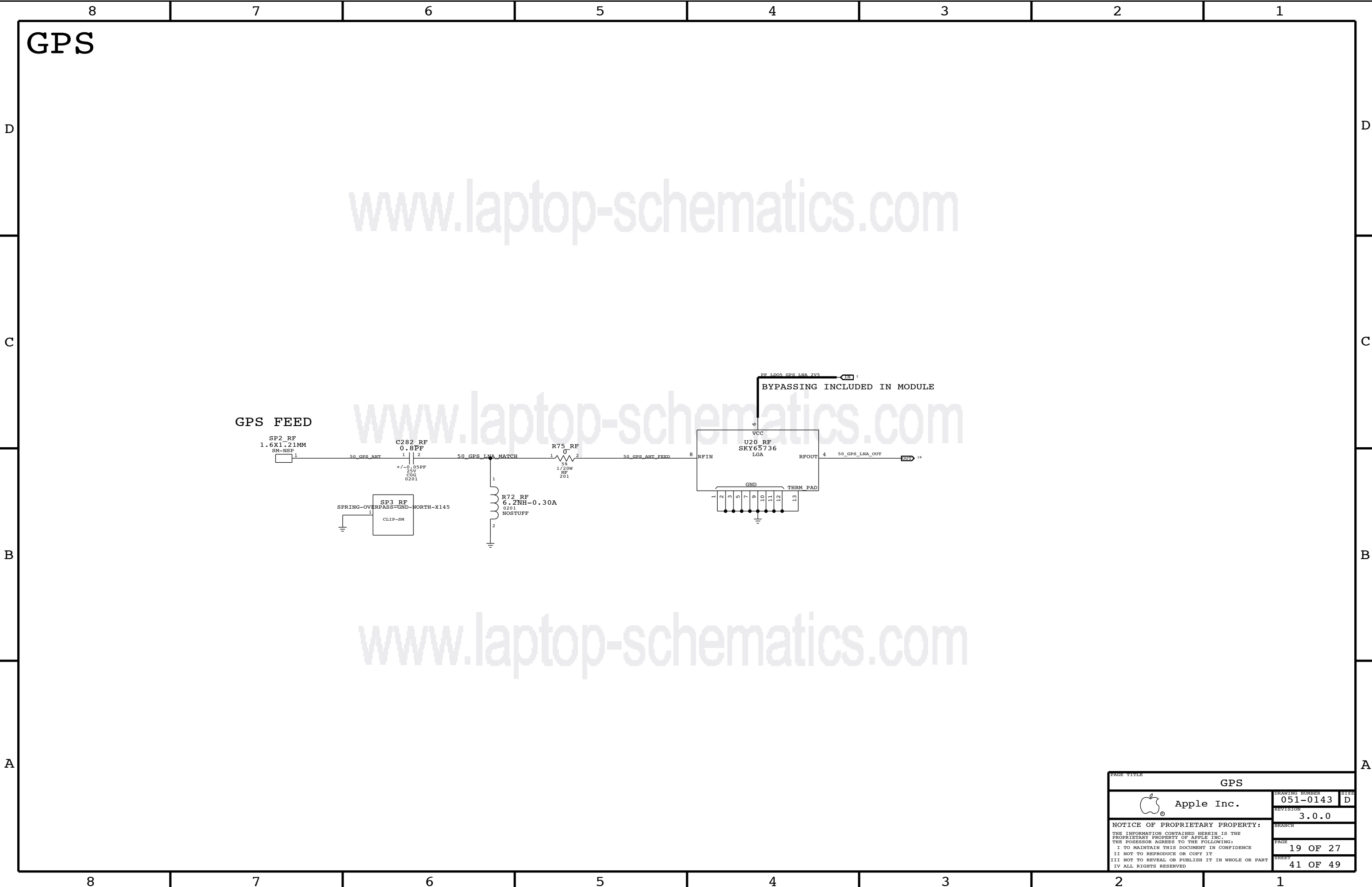


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RX DIVERSITY



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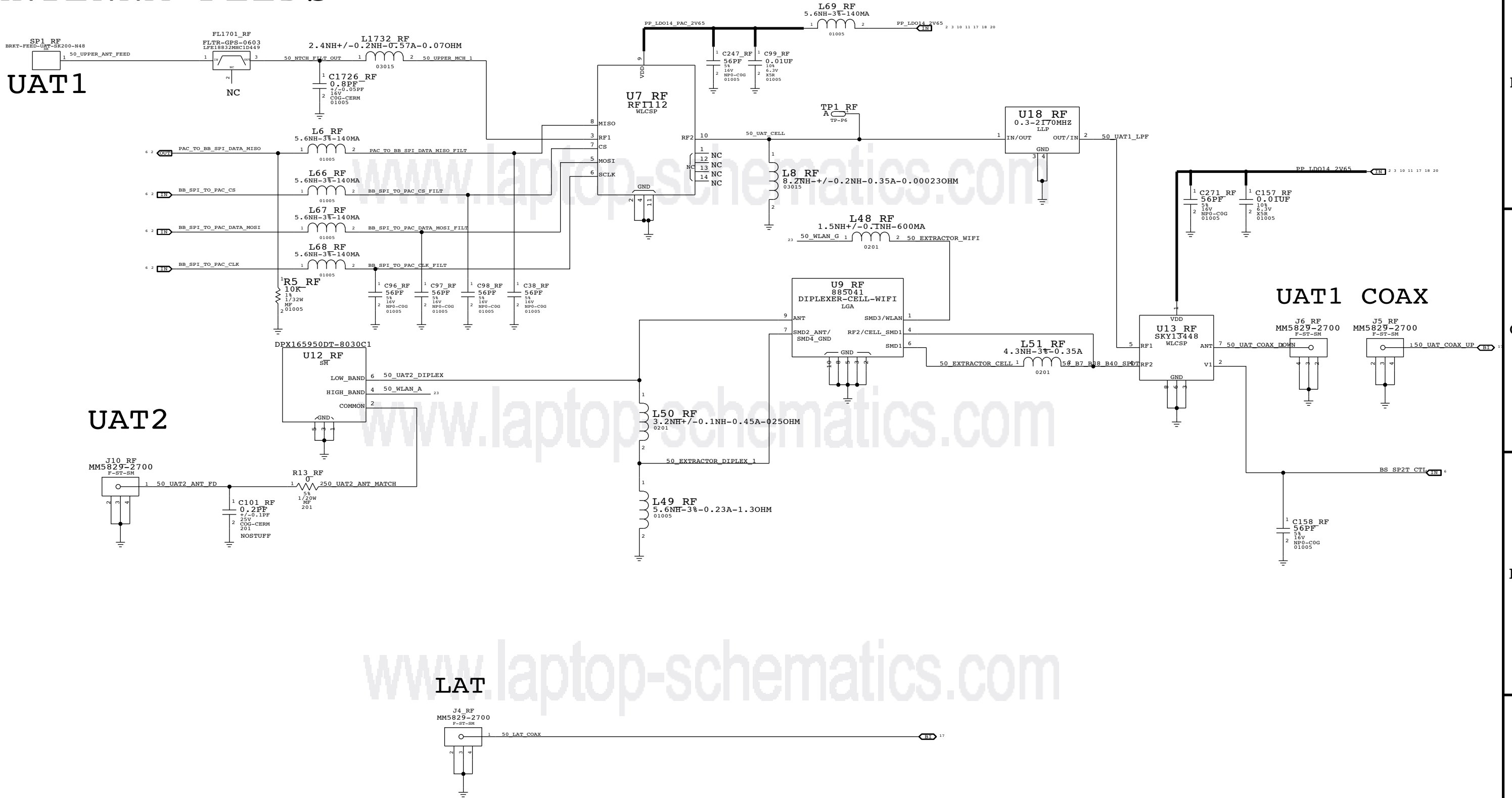
ANTENNA FEEDS

UAT1

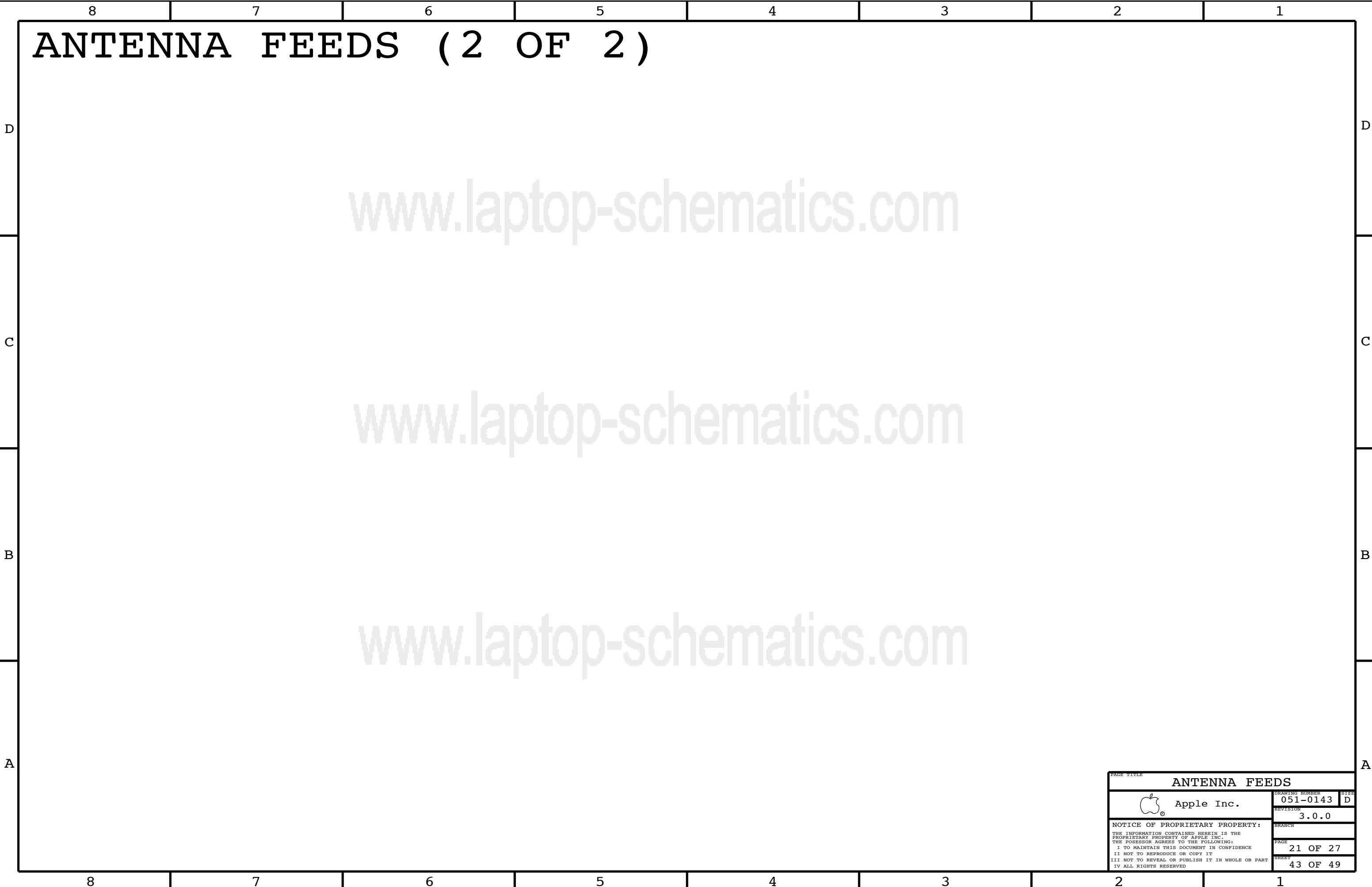
UAT2

LAT

UAT1 COAX



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	FRONT END LOGIC TABLE							
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D	GSM LB TX	LOW	HIGH	LOW	LOW	LOW	LAT	TERMINATED
	GSM LB TX	LOW	HIGH	LOW	LOW	HIGH	UAT	TERMINATED
	GSM HB TX	HIGH	HIGH	LOW	HIGH	LOW	LAT	TERMINATED
	GSM HB TX	HIGH	HIGH	LOW	HIGH	HIGH	UAT	TERMINATED
C	B1	HIGH	HIGH	HIGH	HIGH	LOW	LAT	UAT
	B1	HIGH	HIGH	HIGH	HIGH	HIGH	UAT	LAT
	B2/B25, 1900RX	HIGH	LOW	LOW	HIGH	LOW	LAT	UAT
	B2/B25, 1900RX	HIGH	LOW	LOW	HIGH	HIGH	UAT	LAT
	B3	HIGH	HIGH	LOW	LOW	LOW	LAT	UAT
	B3	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT
	B5/B6/B18, 850RX	HIGH	LOW	LOW	LOW	LOW	LAT	UAT
	B5/B6/B18, 850RX	HIGH	LOW	LOW	LOW	HIGH	UAT	LAT
	B20	HIGH	LOW	HIGH	HIGH	LOW	LAT	UAT
	B20	HIGH	LOW	HIGH	HIGH	HIGH	UAT	LAT
	B34/B39 TX	LOW	LOW	HIGH	HIGH	LOW	LAT	TERMINATED
	B34/B39 TX	LOW	LOW	HIGH	HIGH	HIGH	UAT	TERMINATED
	B34 RX	LOW	LOW	LOW	HIGH	LOW	LAT	UAT
	B34 RX	LOW	LOW	LOW	HIGH	HIGH	UAT	LAT
	B39 RX	LOW	LOW	HIGH	LOW	LOW	LAT	UAT
	B39 RX	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
B	B38/B40 TX	LOW	HIGH	HIGH	LOW	LOW	LAT	TERMINATED
	B38/B40 TX	LOW	HIGH	HIGH	LOW	HIGH	UAT	TERMINATED
	B38 RX	HIGH	LOW	HIGH	LOW	LOW	LAT	UAT
	B38 RX	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT
	B40 RX	HIGH	HIGH	HIGH	LOW	LOW	LAT	UAT
	B40 RX	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT
	B7	LOW	HIGH	HIGH	HIGH	LOW	LAT	UAT
	B7	LOW	HIGH	HIGH	HIGH	HIGH	UAT	LAT
A	B8, GSM900 RX	LOW	HIGH	LOW	HIGH	LOW	LAT	UAT
	B8, GSM900 RX	LOW	HIGH	LOW	HIGH	HIGH	UAT	LAT
	GSM1800 RX	LOW	LOW	LOW	LOW	LOW	LAT	TERMINATED
	GSM1800 RX	LOW	LOW	LOW	LOW	HIGH	UAT	TERMINATED
	LAT = LOWER ANTENNA UAT = UPPER ANTENNA							
	8	7	6	5	4	3	2	1

PAGE TITLE

FRONT END LOGIC TABLE

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The schematic diagram shows the XW9 RF module with two input pins labeled 1 and 2. Pin 1 is connected to PP_VCC_MAIN_WLAN and Pin 2 is connected to PP_BATT_VCC_WLAN. The module is also connected to two capacitors, C103_RF and C104_RF, which are connected to ground. The module is labeled XW9 RF and has a part number SHORT-10L-0725MM-SM.



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